

# IDF2013

英特尔信息技术峰会

## 使用 Wind River Simics\* 虚拟平台加速 固件开发

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### PTAS003

# 议程

- 当前固件 (firmware) 开发的问题
- 虚拟平台的优势
- Wind River (风河) Simics\* 在固件开发中的应用
- Wind River (风河) Simics\* 集成的调试功能
- 总结 / 问答

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该网址同时打印于会议指南中专题讲座日程页的上方

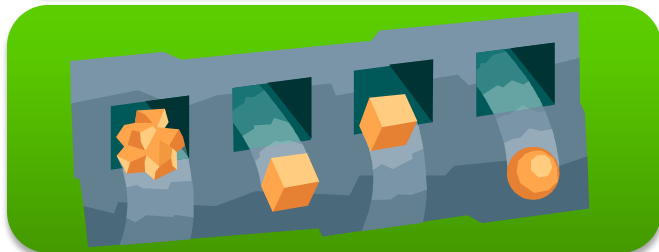


# 当前 固件开发 的问题

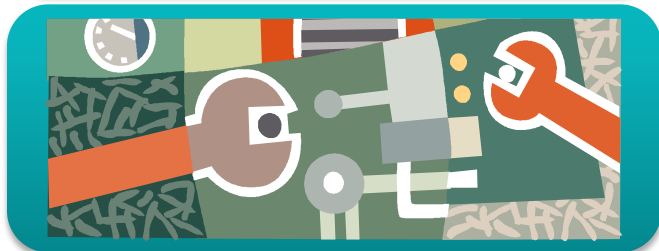
# 当前固件 (firmware) 开发的问题



需要更早地进行固件开发  
特别在硬件延迟的情况下



早期的参考板部分关键功能缺失。  
难以测试

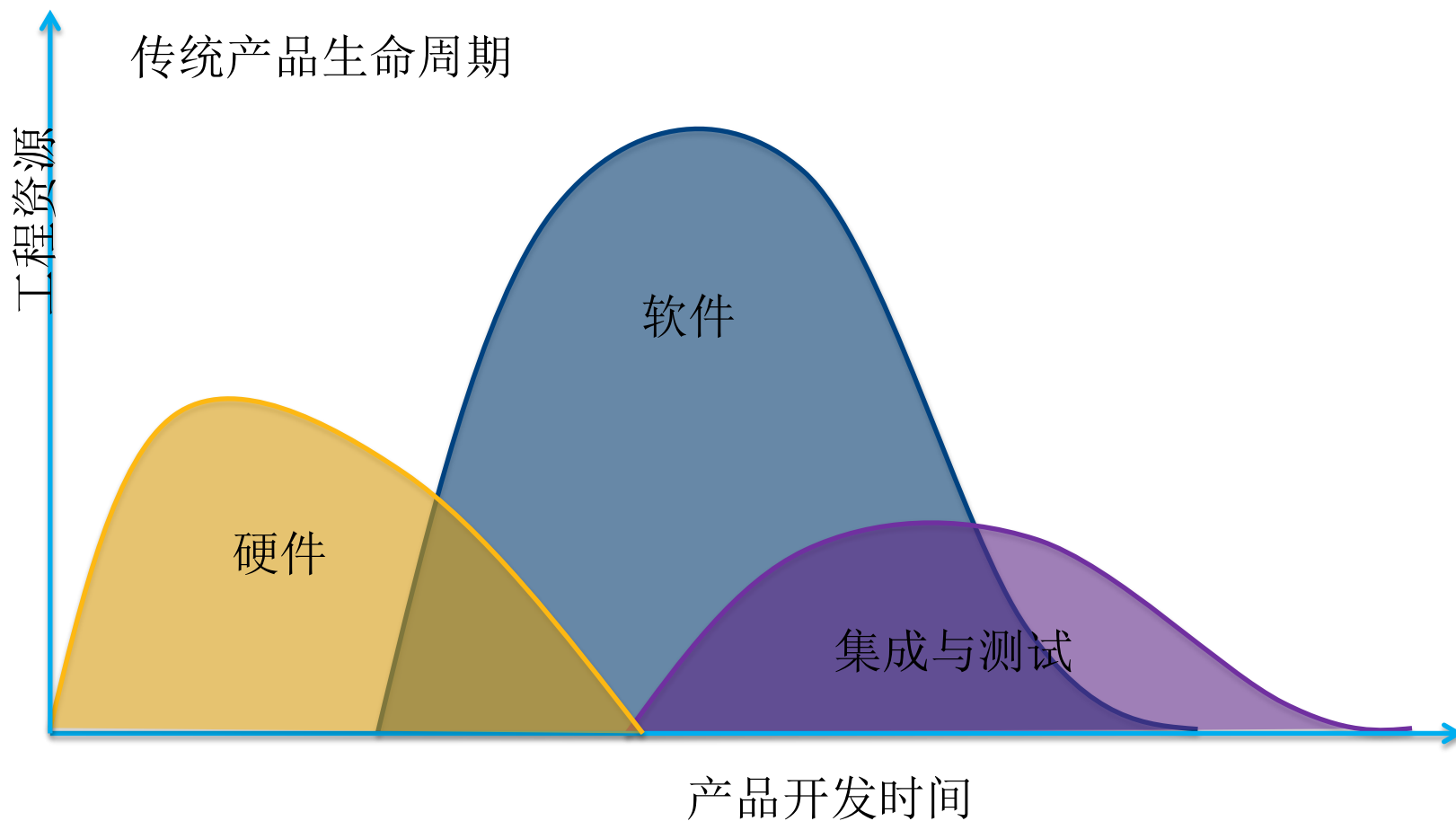


参考板无法修改关键配置  
以作固件测试

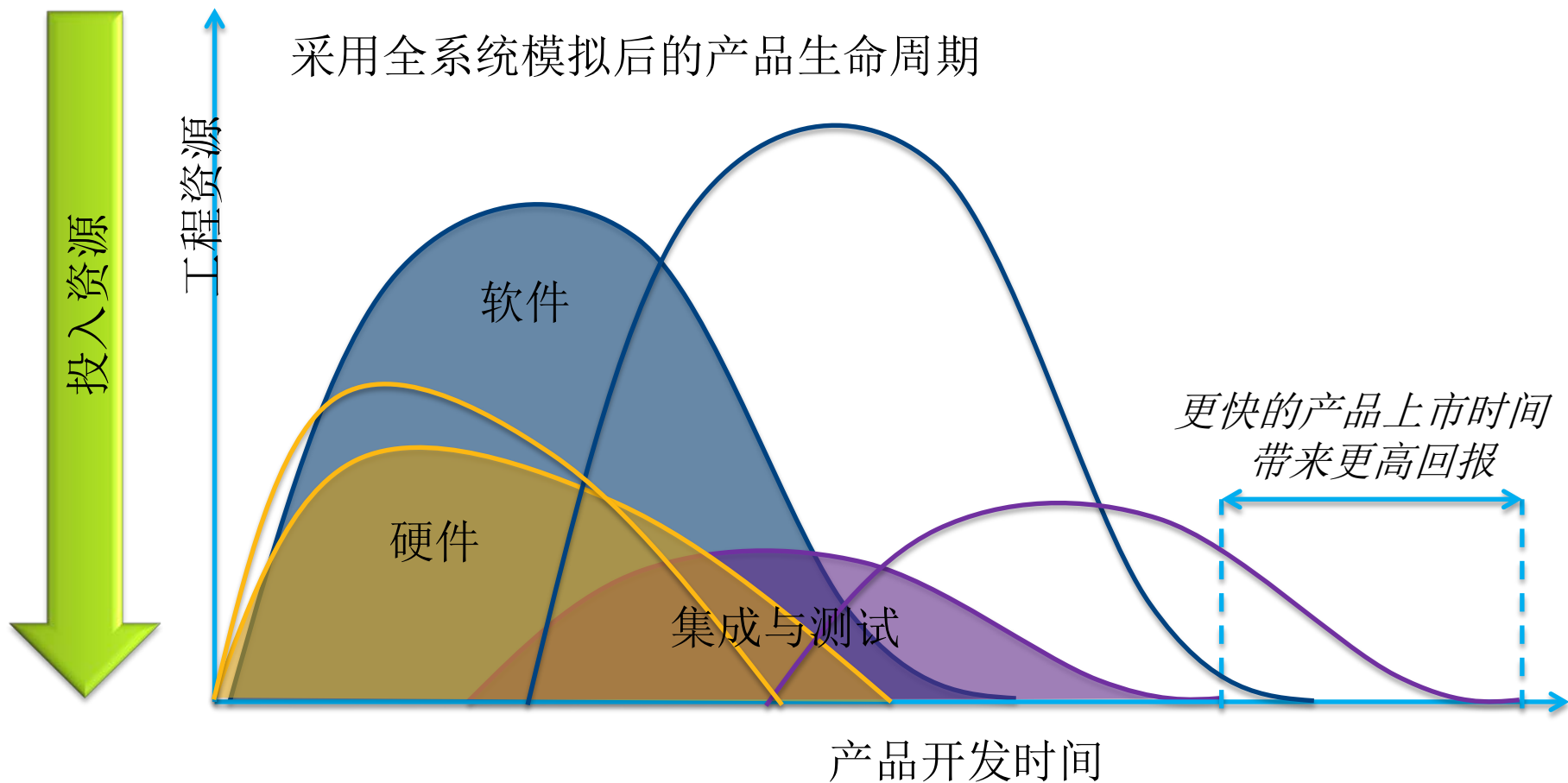


参考板功能尚未稳定  
客户即已要求完整固件

# 左移：加快产品上市速度



# 左移：加快产品上市速度





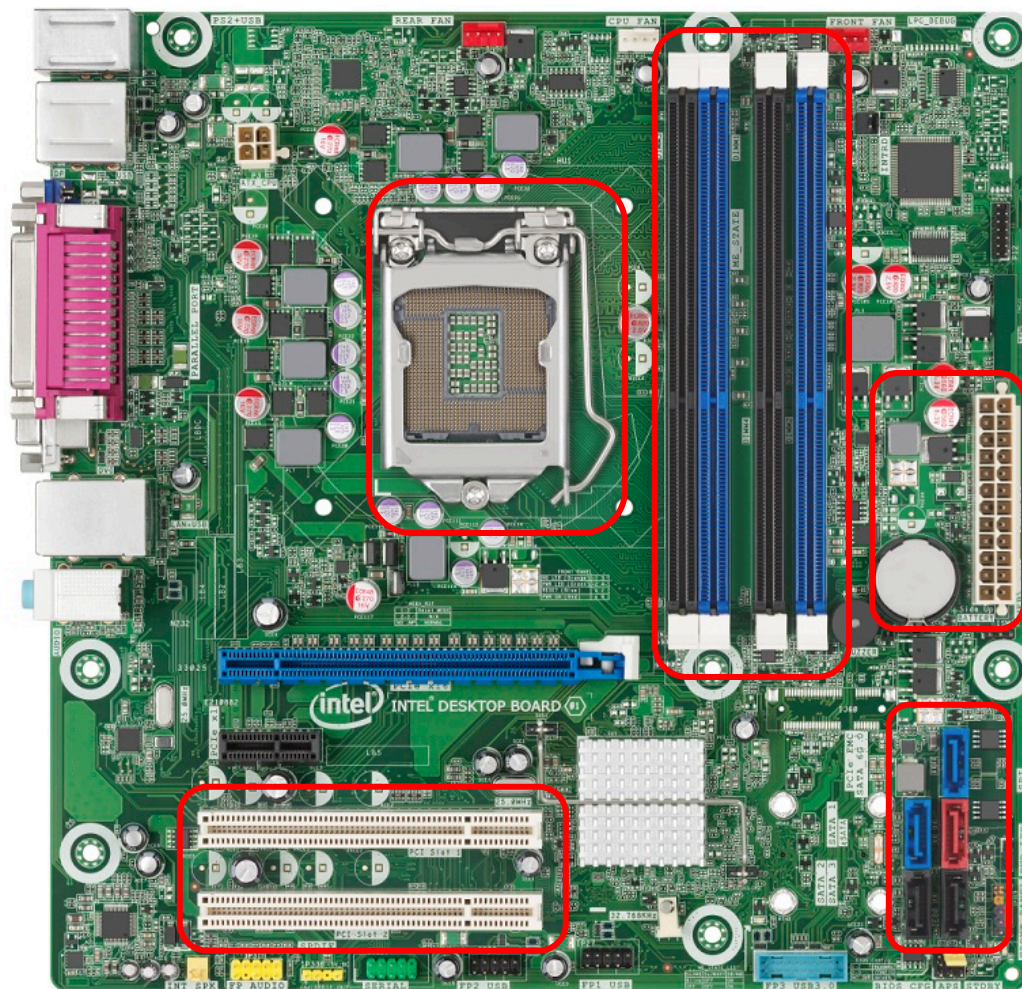
# 启动第一块单板…

所有端口连线无误？

能测试不同的处理器？

该单板能支持不同大小，  
如：最大容量，的内存？

逻辑电平状态稳定  
(电压，地线，校准线)？



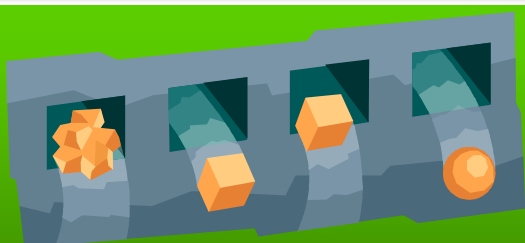
硬件问题往往延迟固件发布



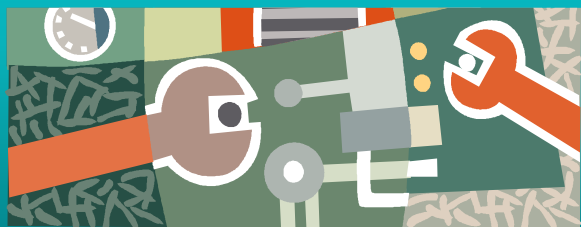
# 固件开发者需要的是…



- 更早可用的平台



- 能运行所有平台特性



- 快速尝试不同平台配置



- 即使硬件不稳定或不可用，也不影响工作

现代固件开发需要超越 “参考板”



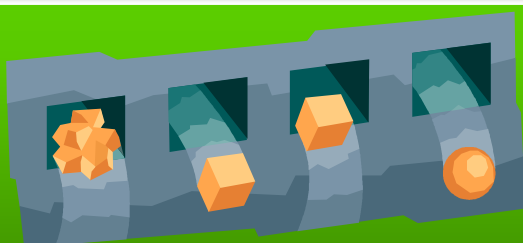


# 虚拟平台 的优势

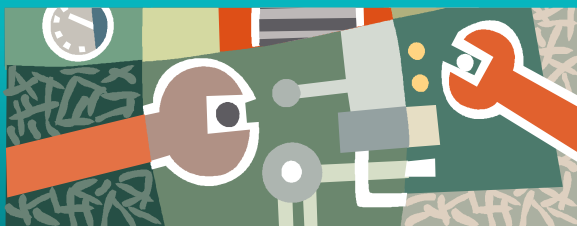
# 虚拟平台的优势



在硬件正式发行前即可用



对所有平台特性建模



通过脚本方便地重配置系统，并进行对各种配置系统的模拟与测试



虚拟平台总是提供稳定的执行环境与可重现的执行结果



# 解决“经典”固件问题

虚拟平台可处理“经典”问题

...

- 用户希望在硬件平台可用之前启动固件
- 第一块单板总有关键功能或特性缺失
- 第一块单板往往不稳定而难以测试
- 早期单板数量有限，难以满足系统固件开发的需要

对英特尔硬件建模的虚拟平台在参考板之前即可用于软件开发

在硬件稳定之前虚拟平台已实现稳定有效的相关特性

虚拟平台实际作为软件使用没有数量限制



# 解决“新”固件问题

重配置虚拟平台来包含虚拟板中未实现的特性

通过虚拟平台处理其它问题…

- 并非每个硬件特性在参考板上皆可运行
- 用户想尝试参考板中并未包含的硬件组合.
- 固件开发周期要求过紧
- 固件就是“魔法”. 她能解决一切问题 😊

利用虚拟平台更早进行固件开发

许多人还是会这么想, 我们不知道为什么 😞



# 通过虚拟平台开发的难点

## 精确度

- 模拟器需要正确建模硬件行为

## 性能


- 不能因为模拟硬件速度过慢反而影响开发

## 调试能力

- 是否提供真实硬件上相应的调试工具与调试能力

虚拟平台能益于固件开发





# 风河Simics\* 在固件开发中的 应用



# 风河 (Wind River) Simics\*是怎样一个工具？

风河Simics\*是一个全系统软件模拟器，供软件开发人员模拟硬件，她能模拟任何规模和复杂度的电子系统。



目标系统

- 模拟任何规模的目标系统
- 运行无需修改的目标代码

Simics\*帮您**突破**传统的产品开发流程



# 能模拟任何电子系统

- (软件)模拟使工程师人手一套系统，不管它多复杂…

…或多板:

通过以太网或其他总线互联



模拟单板:

客户定制板  
或标准成品/参考设计板，  
包含CPU和板上所有外设

…或整个机架:  
通过VME或背板互联

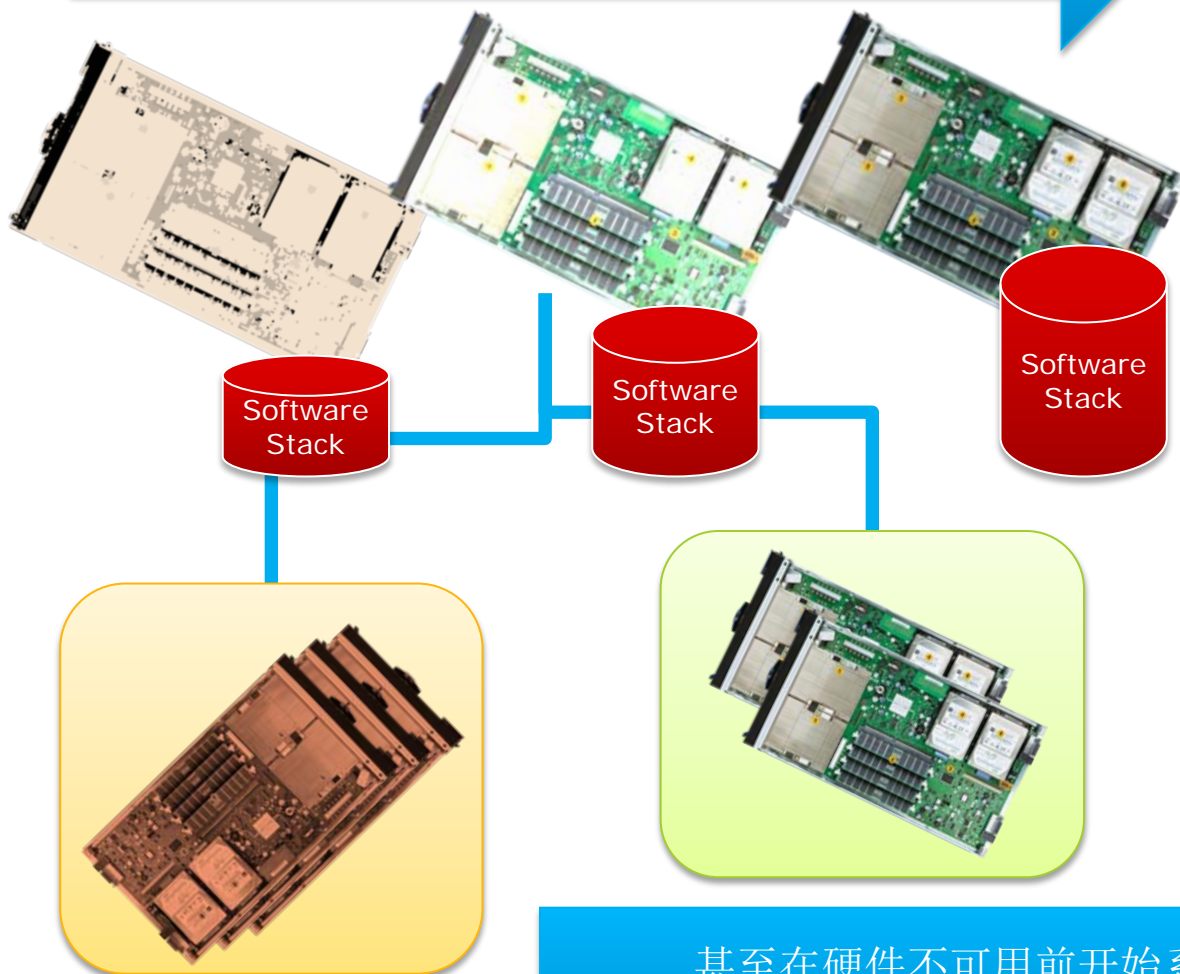


…或是定制完整数字系统:  
包括上百个CPU和外设



# 持续集成

不必推迟软件开发以等待芯片或新板全部完工…  
更早开始

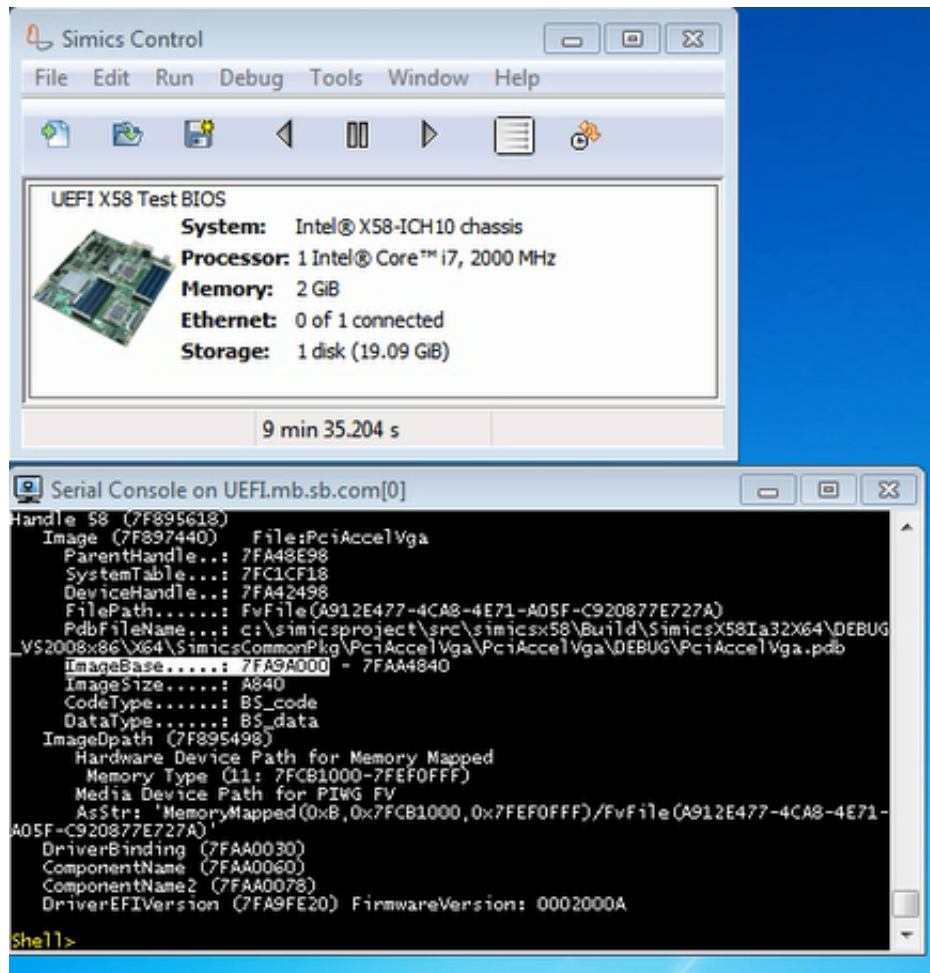


- 全项目周期的持续集成，不用等待硬件
- 减少（小）：
  - 风险
  - 费用
  - 失误
  - 产品上市时间



# Simics\* 的硬件模拟精度

- Simics\* 模型的精确度能用模拟硬件平台上运行的UEFI BIOS 来展示
- 在真实与模拟硬件平台间, 目标代码无需修改
- 目标代码甚至不知道自己是在模拟器上运行



# Simics\* 模拟运行速度

在 Simics\* 模拟硬件上, Microsoft\* Windows\* 7 实际启动<sup>1</sup> 时间约 1 分钟



真实硬件上某些用户系统启动windows 7时间甚至接近30分钟

用户还需要更多时间运行指定应用到相关场景

Microsoft Windows 7 能只约1 秒<sup>1</sup> 即从系统检查点(checkpoint) 恢复

从Simics\* 系统检查点中恢复甚至比传统启动过程更快

<sup>1</sup> 实际启动时间依赖于宿主机资源与所模拟硬件配置。配置不同结果可能亦不同。





# 今日现实：系统开发全球化



开发团队

测试团队

支持团队

如何准确地交流与分享事务，例如系统配置与场景重现步骤？



# 基于系统检查点 (Checkpoint) 协同工作

Simics\* 虚拟平台  
从错误发现处目标系统中提  
取配置

Simics\* 脚本  
自动化触发错误的步骤



测试团队  
发现错误/bug

Simics\* 检查点  
全目标系统快照，可于任  
何地点的其他机器恢复并  
继续运行



开发团队  
读入检查点并恢复执行  
定位错误源



# 在Simics\*虚拟平台中调试软件

Simics\* 固有的调试工具  
可用于 UEFI 开发

为固件开发定制化的外部工具也能集成入Simics\*  
提供更丰富的调试能力

Simics\* 特性满足固件开发需要



# 内部用户使用反馈：

- Simics\* 已被用于UEFI BIOS开发：
  - “... 在使用第一块硬件单板之前三个月就已经被发现和消除了许多BIOS问题，启动那块单板只遇到了一个较小的BIOS软件错误”
  - “... Simics是非常强大的工具，她允许用户对模型和软件做几乎任何调试/修改/替代方案，其它模拟器很难做到”
  - “... 通过Simics在更早的阶段触发并修正了许多BIOS的软件错误，包括一些复杂的ACPI问题，使用几乎没有ACPI问题的BIOS终于能成功地对项目进行立项。”





# 风河 Simics\* 集成的调试功能

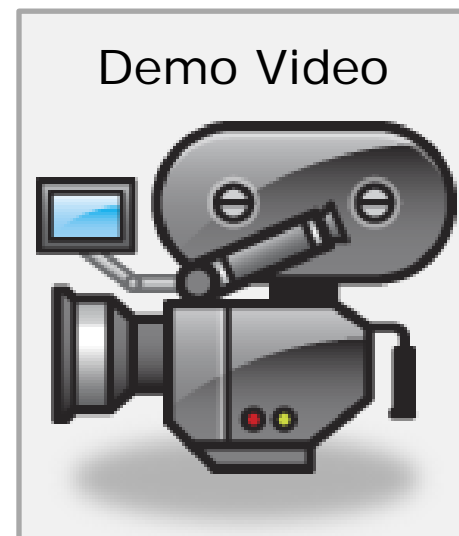
# 风河Simics\* 集成英特尔® ITP 软件

作为例子，Simics\* 集成了英特尔® ITP (In-Target Probe) 软件调试工具

- 与真实硬件使用相同的工具界面/前端
- 工具后端直接使用Simics\*固有的调试功能  
(并不依赖或需要模拟扩展调试端口 - XDP 硬件接口)

示范视频:

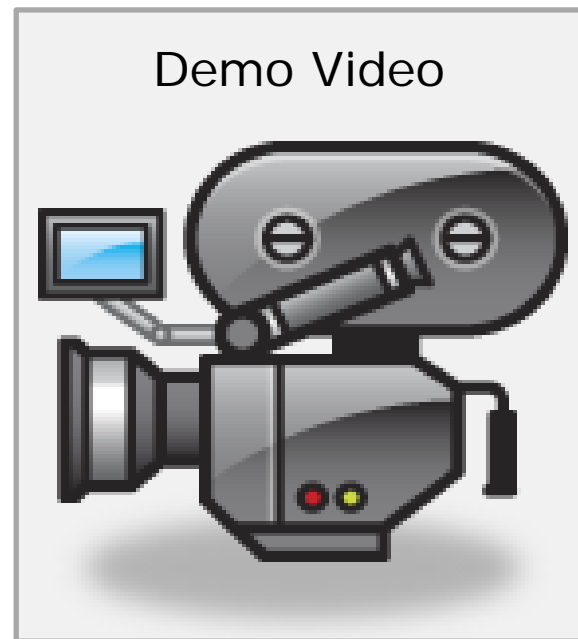
- 启动到UEFI Shell
- 在视频驱动程序中添加断点
- 调试驱动程序中的 BLT 函数



# 在风河Simics\* Eclipse\* 界面中 集成UEFI 调试能力

## 示范视频:

- 视频用例运行于基于英特尔下一代微架构 (codename Haswell) 的Simics\*服务器模型
- 自由设置各类断点, 不受动态模块调用与多遍运行影响.
- 提供执行控制: 单步, 单步跨过 (step over), 函数跳过 (step out), 逆向单步, 逆向单步跨过, 逆向函数跳过 (un-call)
- 调试器内直接源文件编辑
- 检查与调试各种软件变量, 硬件寄存器, 以及UEFI模块
- 可访问所有通用UEFI命令





# 总结

- 现代固件开发需要超越“参考板”
- 虚拟平台能益于固件开发
- Simics\* 特性满足固件开发需要
- Simics\* 提供无缝固件调试



# 更多信息

## UEFI

- UEFI 论坛学习中心
  - [http://www.uefi.org/learning\\_center/](http://www.uefi.org/learning_center/)
- 英特尔UEFI 社区
  - <http://intel.com/udk>
- 使用TianoCore [edk2-devel mailing list](#) 邮件列表得到其它UEFI开发者帮助

## 风河 Simics\*

- <http://www.windriver.com/products/simics/>
- [http://www.intel.com/p/en\\_US/embedded/hwsw/software/simics](http://www.intel.com/p/en_US/embedded/hwsw/software/simics)

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# IDF2013

英特尔信息技术峰会

## 使用 Wind River Simics\* 虚拟平台加速固件开发

Backup Materials



# Problems for Today's Firmware Developer

The "classic" challenges haven't changed...

- Customers want boot firmware before the platform is ready
- The first board is always missing key features
- The first board can be unstable and hard to test
- Firmware developers don't get as many boards as they need

Over time, we have more interesting challenges...

- Not every silicon feature can be exercised on the "reference board"
- Customers want to use hardware combinations that can't be tested on the "reference board"
- Schedules are tighter
- Firmware is "magic" so it will fix everything 😊

# 虚拟化后的益处

## 真实硬件

- 真实行为
- (通常)更快的执行速度

## 模拟器

- 自由调试
- Checkpointing (系统检查点/快照)
- 执行确定性
- 可逆执行
- 脚本控制
- 替代硬件
- 优化的执行速度

# Speed? Really?

- Embedded processors slower than server ones
- Almost reach host speed for x86 on x86 (VMP)
- Complex systems often boot slowly
  - Waiting for slow hardware, mandatory timeouts
  - Clearing memory
  - Hardware self-tests
  - Lots of idle time in parallel systems
- Simics can fast forward when system is waiting!
- Loading software on real system:
  - Program flash memory, load over network or USB
- Loading software on Simics\*:
  - Load binary directly into target memory in no time
- Checkpointing
  - No need to reboot every time