



Intel[®] System Debugger 2019

Release Notes for Linux host*

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Contents

1	Introduction	3
2	Supported Operating Systems	4
3	Supported Platforms	5
4	New in This Release – 2019 Update 4	6
5	Known Issues	7
6	Change History	8
7	Legal Information	9

1 Introduction

This document covers release specific information of all components Intel® System Debugger 2019 for Linux* host including

- Intel® System Debugger - System Debug
- Intel® System Debugger - System Trace

2 Supported Operating Systems

Intel® System Debugger 2019 for Linux* host supports the following operating systems:

2.1 Host Operating Systems

- Fedora* 27
- Ubuntu* 16.04 LTS

2.2 Target Operating Systems

2.2.1 Linux* Target

- Wind River* Linux* 9
- Yocto project* 2.4
- Ubuntu* 16.04
- Wind River* VxWorks* 7

2.2.2 Windows* Target

- Microsoft Windows* 10

3 Supported Platforms

Each Intel® System Debugger tool has its own supported platforms. Furthermore, the tools can provide several probe options for a connection that are

- Intel® In-Target Probe (Intel® ITP) XDP3
- Intel® Silicon View Technology (Intel® SVT) Closed Chassis Adapter (CCA)
- Intel® Direct Connect Interface (Intel® DCI) Debug Class (DbC) cable

The table below lists the platforms and probes supported by each tool of Intel® System Debugger 2019 (Update 4) for Linux* host.

	System Debug			System Trace		
	XDP3	CCA	DbC	XDP3	CCA	DbC
Intel Atom® Processors N4200, N3350, x7-E3950, x5-39xx (Apollo Lake)			✓			✓
Intel Atom® Processor C3xxx (Denverton)			✓			✓
Intel® Pentium® Silver Processor N5XXX, J5XXX or Celeron® Processor N4XXX, J4XXX (Gemini Lake)			✓			✓
Intel Atom® Processor E6xx (Tunnel Creek)	✓					
Intel Atom® Processor Z36xx, Z37xx - 2 cores (Baytrail / MinnowBoard MAX)	✓					
Intel Atom® Processor E3805, E382x, Z3680 - 2 cores (Valleyview)	✓					
Intel Atom® Processor E384x, Z37xx - 4 cores (Valleyview)	✓					

4 New in This Release – 2019 Update 4

- OpenIPC version is updated as 1.1913.3651.100

5 Known Issues

- **Platform security policy may inhibit debugger operation**
 - **Issue:** In some platforms, the security policy may disable JTAG access to the CPU. This is intended to prevent reverse-engineering. In this case the Intel® System Debugger will successfully connect to the target, however it will not be able to discover any CPUs on the JTAG bus and will warn the user that no CPUs are available.
 - **Workaround:** To resolve this issue please ensure that that platform firmware has enabled access to the CPUs via JTAG. This is typically done by flashing a special “debug” firmware into the target. Also note that in some cases CPU or CPU module may have physically disabled JTAG access, especially in production or near-production versions. In this case please work with the platform business unit to obtain a JTAG-enabled hardware.

5.1 Intel® System Debugger – System Debug

- **Intel® System Debugger – System Debug shows a blank window while connecting to Gemini Lake platforms**
 - **Issue:** The tool shows a blank window if Gemini Lake is used as a target platform
 - **Workaround:** The problem occurs at connection level, reconnection to target is needed

6 Change History

6.1 2019 Update 3

- OpenIPC version is updated as 1.1905.3499.100

6.1.1 Intel® System Debugger – System Debug

- Issues with the key shortcuts in Eclipse user interface are fixed

6.2 2019 Update 2

- Intel® System Debugger 2019 Update 2 includes functional and security updates. Users should update to the latest version.

6.2.1 Intel® System Debugger – System Debug

- Migration to new Eclipse (simrel2018-12) and Java11
- Simics demo target is removed

6.3 2019 Update 1

- Target connection editor page is improved, and the size and appearance of the connection dialog wizard are optimized
- OpenIPC version is updated as 1.1839.3251.100

6.3.1 Intel® System Debugger – System Debug

- Implementation of a save button in PCI Tool dialog window
- Fix the bug where user is not able to re-connect to target due to previous session files in the system

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