

TYPE	BANK	R24A Package
Transceiver I/O	10A	68
GPIO	2A	96
GPIO	2B	96
GPIO	2C	96
GPIO	2D	96
GPIO	3A	96
GPIO	3B	96
GPIO	3C	96
GPIO	3D	96
Transceiver I/O	9A	82
HPS I/O	HPS	48
SDM I/O	SDM	29

- i. Total LVDS channels per bank supporting SERDES Non-DPA and DPA mode is equivalent to (LVDS I/O per bank)/2, inclusive of clock pair. Please refer to Dedicated Tx/Rx Channel column in the pin-out table for the channel availability.
- ii. Total LVDS channels supporting SERDES Soft-CDR mode is 12 pairs per bank. Please refer to Soft CDR column in the pin out table for the channel availability.

















Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R2A4	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
3B	84	VREFB3BNO	IO				DIFF_TX_3832p	No	G40	DQ97	DQ48	DQ24	DQ12
3B	83	VREFB3BNO	IO				DIFF_RX_384n	No	D45	DQ98	DQ49	DQ24	DQ12
3B	82	VREFB3BNO	IO				DIFF_RX_384p	No	B45	DQ98	DQ49	DQ24	DQ12
3B	81	VREFB3BNO	IO				DIFF_TX_384n	No	C44	DQ98	DQ49	DQ24	DQ12
3B	80	VREFB3BNO	IO				DIFF_TX_384p	No	A44	DQ98	DQ49	DQ24	DQ12
3B	79	VREFB3BNO	IO				DIFF_RX_385n	No	D43	DQ5n98	DQ49	DQ24	DQ12
3B	78	VREFB3BNO	IO				DIFF_RX_385p	No	B43	DQ5n98	DQ49	DQ24	DQ12
3B	77	VREFB3BNO	IO	PLL_3B_T_CLKOUT1n			DIFF_TX_385n	No	C42	DQ5n99	DQ5n49/CQn49	DQ5n24/CQn24	DQ12
3B	76	VREFB3BNO	IO	PLL_3B_T_CLKOUT1p,PLL_3B_T_CLKOUT1,PLL_3B_T_FB1			DIFF_TX_385p	No	A42	DQ599	DQ5n49/CQ49	DQ5n24/CQ24	DQ12
3B	75	VREFB3BNO	IO	CDR			DIFF_RX_386n	Yes	D41	DQ99	DQ49	DQ24	DQ12
3B	74	VREFB3BNO	IO	RZQ_T_3B_CDR			DIFF_RX_386p	Yes	B41	DQ99	DQ49	DQ24	DQ12
3B	73	VREFB3BNO	IO	CLK_T_3B_1n			DIFF_TX_386n	No	C40	DQ99	DQ49	DQ24	DQ12
3B	72	VREFB3BNO	IO	CLK_T_3B_1p			DIFF_TX_386p	No	A40	DQ99	DQ49	DQ24	DQ12
3B	71	VREFB3BNO	IO	CLK_T_3B_0n			DIFF_RX_387n	No	J38	DQ100	DQ50	DQ25	DQ12
3B	70	VREFB3BNO	IO	CLK_T_3B_0p			DIFF_RX_387p	No	G38	DQ100	DQ50	DQ25	DQ12
3B	69	VREFB3BNO	IO				DIFF_TX_387n	No	H37	DQ100	DQ50	DQ25	DQ12
3B	68	VREFB3BNO	IO				DIFF_TX_387p	No	F37	DQ100	DQ50	DQ25	DQ12
3B	67	VREFB3BNO	IO	PLL_3B_T_CLKOUT0n			DIFF_RX_388n	No	J36	DQ5n100	DQ50	DQ25	DQ12
3B	66	VREFB3BNO	IO	PLL_3B_T_CLKOUT0p,PLL_3B_T_CLKOUT0,PLL_3B_T_FB0			DIFF_RX_388p	No	G36	DQ5100	DQ50	DQ25	DQ12
3B	65	VREFB3BNO	IO				DIFF_TX_388n	No	H35	DQ5n101	DQ5n50/CQn50	DQ25	DQ5n12/CQn12
3B	64	VREFB3BNO	IO	CDR			DIFF_TX_388p	No	F35	DQ5101	DQ550/CQ50	DQ25	DQ512/CQ12
3B	63	VREFB3BNO	IO	CDR			DIFF_RX_389n	Yes	J34	DQ101	DQ50	DQ25	DQ12
3B	62	VREFB3BNO	IO	CDR			DIFF_RX_389p	Yes	G34	DQ101	DQ50	DQ25	DQ12
3B	61	VREFB3BNO	IO				DIFF_TX_389n	No	H33	DQ101	DQ50	DQ25	DQ12
3B	60	VREFB3BNO	IO				DIFF_TX_389p	No	F33	DQ101	DQ50	DQ25	DQ12
3B	59	VREFB3BNO	IO				DIFF_RX_3810n	No	C38	DQ102	DQ51	DQ25	DQ12
3B	58	VREFB3BNO	IO				DIFF_RX_3810p	No	A38	DQ102	DQ51	DQ25	DQ12
3B	57	VREFB3BNO	IO				DIFF_TX_3810n	No	B37	DQ102	DQ51	DQ25	DQ12
3B	56	VREFB3BNO	IO				DIFF_TX_3810p	No	B37	DQ102	DQ51	DQ25	DQ12
3B	55	VREFB3BNO	IO				DIFF_RX_3811n	No	C36	DQ5n102	DQ51	DQ25	DQ12
3B	54	VREFB3BNO	IO				DIFF_RX_3811p	No	A36	DQ5102	DQ51	DQ25	DQ12
3B	53	VREFB3BNO	IO				DIFF_TX_3811n	No	D35	DQ5n103	DQ5n51/CQn51	DQ5n25/CQn25	DQ12
3B	52	VREFB3BNO	IO				DIFF_TX_3811p	No	B35	DQ5103	DQ551/CQ51	DQ525/CQ25	DQ12
3B	51	VREFB3BNO	IO	CDR			DIFF_RX_3812n	Yes	C34	DQ103	DQ51	DQ25	DQ12
3B	50	VREFB3BNO	IO	CDR			DIFF_RX_3812p	Yes	A34	DQ103	DQ51	DQ25	DQ12
3B	49	VREFB3BNO	IO				DIFF_TX_3812n	No	D33	DQ103	DQ51	DQ25	DQ12
3B	48	VREFB3BNO	IO				DIFF_TX_3812p	No	B33	DQ103	DQ51	DQ25	DQ12
3B	47	VREFB3BNO	IO	CDR			DIFF_RX_3813n	Yes	W45	DQ104	DQ52	DQ26	DQ13
3B	46	VREFB3BNO	IO	CDR			DIFF_RX_3813p	Yes	T45	DQ104	DQ52	DQ26	DQ13
3B	45	VREFB3BNO	IO				DIFF_TX_3813n	No	W44	DQ104	DQ52	DQ26	DQ13
3B	44	VREFB3BNO	IO				DIFF_TX_3813p	No	U44	DQ104	DQ52	DQ26	DQ13
3B	43	VREFB3BNO	IO				DIFF_RX_3814n	No	V43	DQ5n104	DQ52	DQ26	DQ13
3B	42	VREFB3BNO	IO				DIFF_RX_3814p	No	T43	DQ5104	DQ52	DQ26	DQ13
3B	41	VREFB3BNO	IO				DIFF_TX_3814n	No	W42	DQ5n105	DQ5n52/CQn52	DQ26	DQ13
3B	40	VREFB3BNO	IO				DIFF_TX_3814p	No	U42	DQ5105	DQ552/CQ52	DQ26	DQ13
3B	39	VREFB3BNO	IO	CDR			DIFF_RX_3815n	Yes	V41	DQ105	DQ52	DQ26	DQ13
3B	38	VREFB3BNO	IO	CDR			DIFF_RX_3815p	Yes	T41	DQ105	DQ52	DQ26	DQ13
3B	37	VREFB3BNO	IO				DIFF_TX_3815n	No	W40	DQ105	DQ52	DQ26	DQ13
3B	36	VREFB3BNO	IO				DIFF_TX_3815p	No	U40	DQ105	DQ52	DQ26	DQ13
3B	35	VREFB3BNO	IO	CDR			DIFF_RX_3816n	Yes	P45	DQ106	DQ53	DQ26	DQ13
3B	34	VREFB3BNO	IO	CDR			DIFF_RX_3816p	Yes	M45	DQ106	DQ53	DQ26	DQ13
3B	33	VREFB3BNO	IO				DIFF_TX_3816n	No	N44	DQ106	DQ53	DQ26	DQ13
3B	32	VREFB3BNO	IO				DIFF_TX_3816p	No	L44	DQ106	DQ53	DQ26	DQ13
3B	31	VREFB3BNO	IO				DIFF_RX_3817n	No	P43	DQ5n106	DQ53	DQ26	DQ13
3B	30	VREFB3BNO	IO				DIFF_RX_3817p	No	M43	DQ5106	DQ53	DQ26	DQ13
3B	29	VREFB3BNO	IO	PLL_3B_B_CLKOUT1n			DIFF_TX_3817n	No	N42	DQ5n107	DQ5n53/CQn53	DQ5n26/CQn26	DQ13
3B	28	VREFB3BNO	IO	PLL_3B_B_CLKOUT1p,PLL_3B_B_CLKOUT1,PLL_3B_B_FB1			DIFF_TX_3817p	No	L42	DQ5107	DQ553/CQ53	DQ5n26/CQ26	DQ13
3B	27	VREFB3BNO	IO	CDR			DIFF_RX_3818n	Yes	P41	DQ107	DQ53	DQ26	DQ13
3B	26	VREFB3BNO	IO	RZQ_B_3B_CDR			DIFF_RX_3818p	Yes	M41	DQ107	DQ53	DQ26	DQ13
3B	25	VREFB3BNO	IO	CLK_B_3B_1n			DIFF_TX_3818n	No	N40	DQ107	DQ53	DQ26	DQ13
3B	24	VREFB3BNO	IO	CLK_B_3B_1p			DIFF_TX_3818p	No	L40	DQ107	DQ53	DQ26	DQ13
3B	23	VREFB3BNO	IO	CLK_B_3B_0n,CDR			DIFF_RX_3819n	Yes	W38	DQ108	DQ54	DQ27	DQ13
3B	22	VREFB3BNO	IO	CLK_B_3B_0p,CDR			DIFF_RX_3819p	Yes	U38	DQ108	DQ54	DQ27	DQ13
3B	21	VREFB3BNO	IO				DIFF_TX_3819n	No	V37	DQ108	DQ54	DQ27	DQ13
3B	20	VREFB3BNO	IO				DIFF_TX_3819p	No	T37	DQ108	DQ54	DQ27	DQ13
3B	19	VREFB3BNO	IO	PLL_3B_B_CLKOUT0n			DIFF_RX_3820n	No	W36	DQ5n108	DQ54	DQ27	DQ13
3B	18	VREFB3BNO	IO	PLL_3B_B_CLKOUT0p,PLL_3B_B_CLKOUT0,PLL_3B_B_FB0			DIFF_RX_3820p	No	U36	DQ5108	DQ54	DQ27	DQ13
3B	17	VREFB3BNO	IO				DIFF_TX_3820n	No	V35	DQ5n109	DQ5n54/CQn54	DQ27	DQ5n13/CQn13
3B	16	VREFB3BNO	IO				DIFF_TX_3820p	No	T35	DQ5109	DQ554/CQ54	DQ27	DQ513/CQ13
3B	15	VREFB3BNO	IO	CDR			DIFF_RX_3821n	Yes	W34	DQ109	DQ54	DQ27	DQ13
3B	14	VREFB3BNO	IO	CDR			DIFF_RX_3821p	Yes	U34	DQ109	DQ54	DQ27	DQ13
3B	13	VREFB3BNO	IO				DIFF_TX_3821n	No	V33	DQ109	DQ54	DQ27	DQ13
3B	12	VREFB3BNO	IO				DIFF_TX_3821p	No	T33	DQ109	DQ54	DQ27	DQ13
3B	11	VREFB3BNO	IO	CDR			DIFF_RX_3822n	Yes	N38	DQ110	DQ55	DQ27	DQ13
3B	10	VREFB3BNO	IO	CDR			DIFF_RX_3822p	Yes	L38	DQ110	DQ55	DQ27	DQ13
3B	9	VREFB3BNO	IO				DIFF_TX_3822n	No	P37	DQ110	DQ55	DQ27	DQ13
3B	8	VREFB3BNO	IO				DIFF_TX_3822p	No	M37	DQ110	DQ55	DQ27	DQ13
3B	7	VREFB3BNO	IO				DIFF_RX_3823n	No	N36	DQ5n110	DQ55	DQ27	DQ13
3B	6	VREFB3BNO	IO				DIFF_RX_3823p	No	L36	DQ5110	DQ55	DQ27	DQ13
3B	5	VREFB3BNO	IO				DIFF_TX_3823n	No	P35	DQ5n111	DQ5n55/CQn55	DQ5n27/CQn27	DQ13
3B	4	VREFB3BNO	IO				DIFF_TX_3823p	No	M35	DQ5111	DQ555/CQ55	DQ527/CQ27	DQ13
3B	3	VREFB3BNO	IO	CDR			DIFF_RX_3824n	Yes	N34	DQ111	DQ55	DQ27	DQ13
3B	2	VREFB3BNO	IO	CDR			DIFF_RX_3824p	Yes	L34	DQ111	DQ55	DQ27	DQ13
3B	1	VREFB3BNO	IO				DIFF_TX_3824n	No	P33	DQ111	DQ55	DQ27	DQ13
3B	0	VREFB3BNO	IO				DIFF_TX_3824p	No	M33	DQ111	DQ55	DQ27	DQ13
3A	95	VREFB3ANO	IO				DIFF_RX_3A1n	No	H59	DQ112	DQ56	DQ28	DQ14
3A	94	VREFB3ANO	IO				DIFF_RX_3A1p	No	F59	DQ112	DQ56	DQ28	DQ14
3A	93	VREFB3ANO	IO				DIFF_TX_3A1n	No	J58	DQ112	DQ56	DQ28	DQ14

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R2AA	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
3A	92	VREFB3ANO	IO				DIFF_TX_3A1p	No	G58	DQ112	DQ56	DQ28	DQ14
3A	91	VREFB3ANO	IO				DIFF_RX_3A2n	No	H57	DQ5n112	DQ56	DQ28	DQ14
3A	90	VREFB3ANO	IO			AVST_READY	DIFF_RX_3A2p	No	F57	DQ5n112	DQ56	DQ28	DQ14
3A	89	VREFB3ANO	IO			AVST_DATA31	DIFF_TX_3A2n	No	J56	DQ5n113	DQ556/CQn56	DQ28	DQ14
3A	88	VREFB3ANO	IO			AVST_DATA30	DIFF_TX_3A2p	No	G56	DQ5n113	DQ556/CQ56	DQ28	DQ14
3A	87	VREFB3ANO	IO	CDR		AVST_DATA29	DIFF_RX_3A3n	Yes	H55	DO113	DQ56	DQ28	DQ14
3A	86	VREFB3ANO	IO	CDR		AVST_DATA28	DIFF_RX_3A3p	Yes	F55	DO113	DQ56	DQ28	DQ14
3A	85	VREFB3ANO	IO			AVST_DATA27	DIFF_TX_3A3n	No	J54	DO113	DQ56	DQ28	DQ14
3A	84	VREFB3ANO	IO			AVST_DATA26	DIFF_TX_3A3p	No	G54	DO113	DQ56	DQ28	DQ14
3A	83	VREFB3ANO	IO			AVST_DATA25	DIFF_RX_3A4n	No	H61	DQ114	DQ57	DQ28	DQ14
3A	82	VREFB3ANO	IO			AVST_DATA24	DIFF_RX_3A4p	No	F61	DQ114	DQ57	DQ28	DQ14
3A	81	VREFB3ANO	IO			AVST_DATA23	DIFF_TX_3A4n	No	D59	DQ114	DQ57	DQ28	DQ14
3A	80	VREFB3ANO	IO			AVST_DATA22	DIFF_TX_3A4p	No	C58	DQ114	DQ57	DQ28	DQ14
3A	79	VREFB3ANO	IO			AVST_DATA21	DIFF_RX_3A5n	No	D57	DQ5n114	DQ57	DQ28	DQ14
3A	78	VREFB3ANO	IO			AVST_DATA20	DIFF_RX_3A5p	No	B57	DQ5n114	DQ57	DQ28	DQ14
3A	77	VREFB3ANO	IO	PLL_3A_T_CLKOUT1n		AVST_DATA19	DIFF_TX_3A5n	No	C56	DQ5n115	DQ5n57/CQn57	DQ5n28/CQn28	DQ14
3A	76	VREFB3ANO	IO	PLL_3A_T_CLKOUT1p,PLL_3A_T_CLKOUT1,PLL_3A_T_FB1		AVST_DATA18	DIFF_TX_3A5p	No	A56	DQ5n115	DQ5n57/CQ57	DQ5n28/CQ28	DQ14
3A	75	VREFB3ANO	IO	CDR		AVST_DATA17	DIFF_RX_3A6n	Yes	D55	DO115	DQ57	DQ28	DQ14
3A	74	VREFB3ANO	IO	RQZ_T_3A_CDR		AVST_DATA16	DIFF_RX_3A6p	Yes	B55	DO115	DQ57	DQ28	DQ14
3A	73	VREFB3ANO	IO	CLK_T_3A_1n			DIFF_TX_3A6n	No	C54	DO115	DQ57	DQ28	DQ14
3A	72	VREFB3ANO	IO	CLK_T_3A_1p			DIFF_TX_3A6p	No	A54	DO115	DQ57	DQ28	DQ14
3A	71	VREFB3ANO	IO	CLK_T_3A_0n			DIFF_RX_3A7n	No	J52	DQ116	DQ58	DQ29	DQ14
3A	70	VREFB3ANO	IO	CLK_T_3A_0p			DIFF_RX_3A7p	No	G52	DQ116	DQ58	DQ29	DQ14
3A	69	VREFB3ANO	IO				DIFF_TX_3A7n	No	H51	DQ116	DQ58	DQ29	DQ14
3A	68	VREFB3ANO	IO				DIFF_TX_3A7p	No	F51	DQ116	DQ58	DQ29	DQ14
3A	67	VREFB3ANO	IO	PLL_3A_T_CLKOUT0n			DIFF_RX_3A8n	No	J50	DQ5n116	DQ58	DQ29	DQ14
3A	66	VREFB3ANO	IO	PLL_3A_T_CLKOUT0p,PLL_3A_T_CLKOUT0,PLL_3A_T_FB0			DIFF_RX_3A8p	No	G50	DQ5n116	DQ58	DQ29	DQ14
3A	65	VREFB3ANO	IO			AVST_CLK	DIFF_TX_3A9n	No	H49	DQ5n117	DQ5n58/CQn58	DQ29	DQ5n14/CQn14
3A	64	VREFB3ANO	IO	CDR		AVST_DATA15	DIFF_TX_3A9n	No	F49	DQ5n117	DQ5n58/CQ58	DQ29	DQ5n14/CQ14
3A	63	VREFB3ANO	IO	CDR		AVST_DATA14	DIFF_RX_3A9n	Yes	J48	DQ117	DQ58	DQ29	DQ14
3A	62	VREFB3ANO	IO	CDR		AVST_DATA13	DIFF_RX_3A9p	Yes	G48	DQ117	DQ58	DQ29	DQ14
3A	61	VREFB3ANO	IO			AVST_DATA12	DIFF_TX_3A9n	No	H47	DQ117	DQ58	DQ29	DQ14
3A	60	VREFB3ANO	IO			AVST_DATA11	DIFF_TX_3A9p	No	F47	DQ117	DQ58	DQ29	DQ14
3A	59	VREFB3ANO	IO			AVST_DATA10	DIFF_RX_3A10n	No	C52	DQ118	DQ59	DQ29	DQ14
3A	58	VREFB3ANO	IO			AVST_DATA9	DIFF_RX_3A10p	No	A52	DQ118	DQ59	DQ29	DQ14
3A	57	VREFB3ANO	IO			AVST_DATA8	DIFF_TX_3A10n	No	D51	DO118	DQ59	DQ29	DQ14
3A	56	VREFB3ANO	IO			AVST_VALID	DIFF_TX_3A10p	No	B51	DO118	DQ59	DQ29	DQ14
3A	55	VREFB3ANO	IO			AVST_DATA7	DIFF_RX_3A11n	No	DQ5n118	DQ59	DQ29	DQ14	
3A	54	VREFB3ANO	IO			AVST_DATA6	DIFF_RX_3A11p	No	A50	DQ5n118	DQ59	DQ29	DQ14
3A	53	VREFB3ANO	IO			AVST_DATA5	DIFF_TX_3A11n	No	D49	DQ5n119	DQ5n59/CQn59	DQ5n29/CQn29	DQ14
3A	52	VREFB3ANO	IO			AVST_DATA4	DIFF_TX_3A11p	No	B49	DQ5n119	DQ5n59/CQ59	DQ5n29/CQ29	DQ14
3A	51	VREFB3ANO	IO	CDR		AVST_DATA3	DIFF_RX_3A12n	Yes	C48	DQ119	DQ59	DQ29	DQ14
3A	50	VREFB3ANO	IO	CDR		AVST_DATA2	DIFF_RX_3A12p	Yes	A48	DQ119	DQ59	DQ29	DQ14
3A	49	VREFB3ANO	IO			AVST_DATA1	DIFF_TX_3A12n	No	D47	DQ119	DQ59	DQ29	DQ14
3A	48	VREFB3ANO	IO			AVST_DATA0	DIFF_TX_3A12p	No	B47	DQ119	DQ59	DQ29	DQ14
3A	47	VREFB3ANO	IO	CDR			DIFF_RX_3A13n	Yes	V59	DO120	DQ60	DQ30	DO15
3A	46	VREFB3ANO	IO	CDR			DIFF_RX_3A13p	Yes	T59	DO120	DQ60	DQ30	DO15
3A	45	VREFB3ANO	IO				DIFF_TX_3A13n	No	W58	DO120	DQ60	DQ30	DO15
3A	44	VREFB3ANO	IO				DIFF_TX_3A13p	No	U58	DO120	DQ60	DQ30	DO15
3A	43	VREFB3ANO	IO				DIFF_RX_3A14n	No	V57	DQ5n120	DQ60	DQ30	DO15
3A	42	VREFB3ANO	IO				DIFF_RX_3A14p	No	T57	DQ5n120	DQ60	DQ30	DO15
3A	41	VREFB3ANO	IO				DIFF_TX_3A14n	No	W56	DQ5n121	DQ5n60/CQn60	DQ30	DO15
3A	40	VREFB3ANO	IO				DIFF_TX_3A14p	No	U56	DQ5n121	DQ560/CQ60	DQ30	DO15
3A	39	VREFB3ANO	IO	CDR			DIFF_RX_3A15n	Yes	V55	DQ121	DQ60	DQ30	DO15
3A	38	VREFB3ANO	IO	CDR			DIFF_RX_3A15p	Yes	T55	DQ121	DQ60	DQ30	DO15
3A	37	VREFB3ANO	IO				DIFF_TX_3A15n	No	W54	DO121	DQ60	DQ30	DO15
3A	36	VREFB3ANO	IO				DIFF_TX_3A15p	No	U54	DO121	DQ60	DQ30	DO15
3A	35	VREFB3ANO	IO	CDR			DIFF_RX_3A16n	Yes	P59	DQ122	DQ61	DQ30	DO15
3A	34	VREFB3ANO	IO	CDR			DIFF_RX_3A16p	Yes	M59	DQ122	DQ61	DQ30	DO15
3A	33	VREFB3ANO	IO				DIFF_TX_3A16n	No	N58	DQ122	DQ61	DQ30	DO15
3A	32	VREFB3ANO	IO				DIFF_TX_3A16p	No	L58	DQ122	DQ61	DQ30	DO15
3A	31	VREFB3ANO	IO				DIFF_RX_3A17n	No	P57	DQ5n122	DQ61	DQ30	DO15
3A	30	VREFB3ANO	IO				DIFF_RX_3A17p	No	M57	DQ5n122	DQ61	DQ30	DO15
3A	29	VREFB3ANO	IO	PLL_3A_B_CLKOUT1n			DIFF_TX_3A17n	No	N56	DQ5n123	DQ5n61/CQn61	DQ5n30/CQn30	DO15
3A	28	VREFB3ANO	IO	PLL_3A_B_CLKOUT1p,PLL_3A_B_CLKOUT1,PLL_3A_B_FB1			DIFF_TX_3A17p	No	L56	DQ5n123	DQ561/CQ61	DQ5n30/CQ30	DO15
3A	27	VREFB3ANO	IO	CDR			DIFF_RX_3A18n	Yes	P55	DO123	DQ61	DQ30	DO15
3A	26	VREFB3ANO	IO	RQZ_B_3A_CDR			DIFF_RX_3A18p	Yes	M55	DO123	DQ61	DQ30	DO15
3A	25	VREFB3ANO	IO	CLK_B_3A_1n			DIFF_TX_3A18n	No	N54	DO123	DQ61	DQ30	DO15
3A	24	VREFB3ANO	IO	CLK_B_3A_1p			DIFF_TX_3A18p	No	L54	DO123	DQ61	DQ30	DO15
3A	23	VREFB3ANO	IO	CLK_B_3A_0n,CDR			DIFF_RX_3A19n	Yes	W52	DQ124	DQ62	DQ31	DO15
3A	22	VREFB3ANO	IO	CLK_B_3A_0p,CDR			DIFF_RX_3A19p	Yes	U52	DQ124	DQ62	DQ31	DO15
3A	21	VREFB3ANO	IO				DIFF_TX_3A19n	No	V51	DQ124	DQ62	DQ31	DO15
3A	20	VREFB3ANO	IO				DIFF_TX_3A19p	No	T51	DQ124	DQ62	DQ31	DO15
3A	19	VREFB3ANO	IO	PLL_3A_B_CLKOUT0n			DIFF_RX_3A20n	No	W50	DQ5n124	DQ62	DQ31	DO15
3A	18	VREFB3ANO	IO	PLL_3A_B_CLKOUT0p,PLL_3A_B_CLKOUT0,PLL_3A_B_FB0			DIFF_RX_3A20p	No	U50	DQ5n124	DQ62	DQ31	DO15
3A	17	VREFB3ANO	IO				DIFF_TX_3A20n	No	V49	DQ5n125	DQ5n62/CQn62	DQ31	DQ5n15/CQn15
3A	16	VREFB3ANO	IO				DIFF_TX_3A20p	No	T49	DQ5n125	DQ562/CQ62	DQ31	DQ5n15/CQ15
3A	15	VREFB3ANO	IO	CDR			DIFF_RX_3A21n	Yes	W48	DO125	DQ62	DQ31	DO15
3A	14	VREFB3ANO	IO	CDR			DIFF_RX_3A21p	Yes	U48	DO125	DQ62	DQ31	DO15
3A	13	VREFB3ANO	IO				DIFF_TX_3A21n	No	V47	DQ125	DQ62	DQ31	DO15
3A	12	VREFB3ANO	IO				DIFF_TX_3A21p	No	T47	DQ125	DQ62	DQ31	DO15
3A	11	VREFB3ANO	IO	CDR			DIFF_RX_3A22n	Yes	N52	DQ126	DQ63	DQ31	DO15
3A	10	VREFB3ANO	IO	CDR			DIFF_RX_3A22p	Yes	L52	DQ126	DQ63	DQ31	DO15
3A	9	VREFB3ANO	IO				DIFF_TX_3A22n	No	P51	DQ126	DQ63	DQ31	DO15
3A	8	VREFB3ANO	IO				DIFF_TX_3A22p	No	M51	DQ126	DQ63	DQ31	DO15
3A	7	VREFB3ANO	IO				DIFF_RX_3A23n	No	N50	DQ5n126	DQ63	DQ31	DO15
3A	6	VREFB3ANO	IO				DIFF_RX_3A23p	No	L50	DQ5n126	DQ63	DQ31	DO15
3A	5	VREFB3ANO	IO				DIFF_TX_3A23n	No	P49	DQ5n127	DQ5n63/CQn63	DQ5n31/CQn31	DO15

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
3A		4	VREFB3ANO	IO			DIFF_TX_3A23p	No	M49	DQS127	DQS63/CO63	DQS31/CO31	DQ15
3A		3	VREFB3ANO	IO	CDR		DIFF_RX_3A24n	Yes	N48	DQ127	DQ63	DQ31	DQ15
3A		2	VREFB3ANO	IO	CDR		DIFF_RX_3A24p	Yes	L48	DQ127	DQ63	DQ31	DQ15
3A		1	VREFB3ANO	IO			DIFF_TX_3A24n	No	P47	DQ127	DQ63	DQ31	DQ15
3A		0	VREFB3ANO	IO			DIFF_TX_3A24p	No	M47	DQ127	DQ63	DQ31	DQ15
HPS			HPS_IOA_1		GPIO0_I00.SPIM0_S51_N.SPIS0_CLK.UART0_CTS_N.NAND_ADO0.USB0_CLK.SDMMC_CCLK	HPS_IOA_1			AH5				
HPS			HPS_IOA_2		GPIO0_I01.SPIM1_S51_N.SPIS0_MOSI.UART0_RTS_N.NAND_ADO1.USB0_STP.SDMMC_CMD	HPS_IOA_2			AD1				
HPS			HPS_IOA_3		GPIO0_I02.SPIS0_S50_N.UART0_TX.I2C1_SDA.NAND_WE_N.USB0_DIR.SDMMC_DATA0	HPS_IOA_3			AG6				
HPS			HPS_IOA_4		GPIO0_I03.SPIS0_MISO.UART0_RX.I2C1_SCL.NAND_RE_N.USB0_DATA0.SDMMC_DATA1	HPS_IOA_4			AB1				
HPS			HPS_IOA_5		GPIO0_I04.SPIM0_CLK.UART1_CTS_N.I2C0_SDA.NAND_WP_N.USB0_DATA1.SDMMC_DATA2	HPS_IOA_5			AG4				
HPS			HPS_IOA_6		GPIO0_I05.SPIM0_MOSI.UART1_RTS_N.I2C0_SCL.NAND_ADO2.USB0_NXT.SDMMC_DATA3	HPS_IOA_6			AD3				
HPS			HPS_IOA_7		GPIO0_I06.SPIM0_MISO.MDIO2_MDIO.UART1_TX.I2C_EMAC2_SDA.NAND_ADO3.USB0_DATA2.SDMMC_DATA4	HPS_IOA_7			AF5				
HPS			HPS_IOA_8		GPIO0_I07.SPIM0_S50_N.MDIO2_MDC.UART1_RX.I2C_EMAC2_SCL.NAND_CLE.USB0_DATA3.SDMMC_DATA5	HPS_IOA_8			AC2				
HPS			HPS_IOA_9		GPIO0_I08.SPIM1_CLK.MDIO1_MDIO.I2C_EMAC1_SDA.NAND_ADO4.USB0_DATA4.SDMMC_DATA6	HPS_IOA_9			AF2				
HPS			HPS_IOA_10		GPIO0_I09.SPIM1_MOSI.SPIS1_MOSI.MDIO1_MDC.I2C_EMAC1_SCL.NAND_ADO5.USB0_DATA5.SDMMC_DATA7	HPS_IOA_10			AB3				
HPS			HPS_IOA_11		GPIO0_I010.SPIM1_MISO.SPIS1_S50_N.MDIO0_MDIO.I2C_EMAC0_SDA.NAND_ADO6.USB0_DATA6	HPS_IOA_11			AF3				
HPS			HPS_IOA_12		GPIO0_I011.SPIM1_S50_N.SPIS1_MISO.MDIO0_MDC.I2C_EMAC0_SCL.NAND_ADO7.USB0_DATA7	HPS_IOA_12			AA2				
HPS			HPS_IOA_13		GPIO0_I012.NAND_ALE.USB1_CLK.EMAC0_TX_CLK	HPS_IOA_13			AC4				
HPS			HPS_IOA_14		GPIO0_I013.NAND_RB.USB1_STP.EMAC0_TX_CTL	HPS_IOA_14			V1				
HPS			HPS_IOA_15		GPIO0_I014.NAND_CE_N.USB1_DIR.EMAC0_RX_CLK	HPS_IOA_15			AA4				
HPS			HPS_IOA_16		GPIO0_I015.USB1_DATA0.EMAC0_RX_CTL	HPS_IOA_16			T1				
HPS			HPS_IOA_17		GPIO0_I016.NAND_ADO8.USB1_DATA1.EMAC0_TXD0	HPS_IOA_17			AD5				
HPS			HPS_IOA_18		GPIO0_I017.NAND_ADO9.USB1_NXT.EMAC0_TXD1	HPS_IOA_18			P1				
HPS			HPS_IOA_19		GPIO0_I018.NAND_ADO10.USB1_DATA2.EMAC0_RXD0	HPS_IOA_19			AF7				
HPS			HPS_IOA_20		GPIO0_I019.SPIM1_S51_N.NAND_ADO11.USB1_DATA3.EMAC0_RXD1	HPS_IOA_20			M1				
HPS			HPS_IOA_21		GPIO0_I020.SPIM1_CLK.SPIS0_CLK.UART0_CTS_N.I2C1_SDA.NAND_ADO12.USB1_DATA4.EMAC0_TXD2	HPS_IOA_21			AF9				
HPS			HPS_IOA_22		GPIO0_I021.SPIM1_MOSI.SPIS0_MOSI.UART0_RTS_N.I2C1_SCL.NAND_ADO13.USB1_DATA5.EMAC0_TXD3	HPS_IOA_22			W2				
HPS			HPS_IOA_23		GPIO0_I022.SPIM1_MISO.SPIS0_S50_N.UART0_TX.I2C0_SDA.NAND_ADO14.USB1_DATA6.EMAC0_RXD2	HPS_IOA_23			AB5				
HPS			HPS_IOA_24		GPIO0_I023.SPIM1_S50_N.SPIS0_MISO.UART0_RX.I2C0_SCL.NAND_ADO15.USB1_DATA7.EMAC0_RXD3	HPS_IOA_24			U2				
HPS			HPS_I0B_1		GPIO1_I00.SPIM1_CLK.UART0_CTS_N.NAND_ADO0.EMAC1_TX_CLK	HPS_I0B_1			AC6				
HPS			HPS_I0B_2		GPIO1_I01.SPIM1_MOSI.UART0_RTS_N.NAND_ADO1.EMAC1_TX_CTL	HPS_I0B_2			H1				
HPS			HPS_I0B_3		GPIO1_I02.SPIM1_MISO.UART0_TX.I2C0_SDA.NAND_WE_N.EMAC1_RX_CLK	HPS_I0B_3			AA6				
HPS			HPS_I0B_4		GPIO1_I03.SPIM1_S50_N.UART0_RX.I2C0_SCL.NAND_RE_N.EMAC1_RX_CTL	HPS_I0B_4			F1				
HPS			HPS_I0B_5		GPIO1_I04.SPIM1_S51_N.SPIS1_CLK.UART1_CTS_N.NAND_WP_N.EMAC1_TXD0	HPS_I0B_5			AD7				
HPS			HPS_I0B_6		GPIO1_I05.SPIS1_MOSI.UART1_RTS_N.NAND_ADO2.EMAC1_TXD1	HPS_I0B_6			N2				
HPS			HPS_I0B_7		GPIO1_I06.SPIS1_S50_N.UART1_TX.I2C1_SDA.NAND_ADO3.EMAC1_RXD0	HPS_I0B_7			AB7				
HPS			HPS_I0B_8		GPIO1_I07.SPIS1_MISO.UART1_RX.I2C1_SCL.NAND_CLE.EMAC1_RXD1	HPS_I0B_8			L2				
HPS			HPS_I0B_9		GPIO1_I08.JTAG_TCK.SPIS0_CLK.MDIO2_MDIO.I2C_EMAC2_SDA.NAND_ADO4.EMAC1_TXD2	HPS_I0B_9			AC8				
HPS			HPS_I0B_10		GPIO1_I09.JTAG_TMS.SPIS0_MOSI.MDIO2_MDC.I2C_EMAC2_SCL.NAND_ADO5.EMAC1_TXD3	HPS_I0B_10			I2				
HPS			HPS_I0B_11		GPIO1_I010.JTAG_TDO.SPIS0_S50_N.MDIO0_MDIO.I2C_EMAC0_SDA.NAND_ADO6.EMAC1_RXD2	HPS_I0B_11			AA8				
HPS			HPS_I0B_12		GPIO1_I011.JTAG_TDI.SPIS0_MISO.MDIO0_MDC.I2C_EMAC0_SCL.NAND_ADO7.EMAC1_RXD3	HPS_I0B_12			G2				
HPS			HPS_I0B_13		GPIO1_I012.I2C1_SDA.NAND_ALE.SDMMC_CMD.EMAC2_TX_CLK	HPS_I0B_13			AD9				
HPS			HPS_I0B_14		GPIO1_I013.I2C1_SCL.NAND_RB.SDMMC_DATA0.EMAC2_TX_CTL	HPS_I0B_14			V3				
HPS			HPS_I0B_15		GPIO1_I014.UART1_TX.NAND_CE_N.SDMMC_CCLK.EMAC2_RX_CLK	HPS_I0B_15			AB9				
HPS			HPS_I0B_16		GPIO1_I015.UART1_RX.SDMMC_DATA1.EMAC2_RX_CTL	HPS_I0B_16			T3				
HPS			HPS_I0B_17		GPIO1_I016.UART1_CTS_N.NAND_ADO8.SDMMC_DATA2.EMAC2_TXD0	HPS_I0B_17			AC10				
HPS			HPS_I0B_18		GPIO1_I017.SPIM0_S51_N.UART1_RTS_N.NAND_ADO9.SDMMC_DATA3.EMAC2_TXD1	HPS_I0B_18			PF1				
HPS			HPS_I0B_19		GPIO1_I018.SPIM0_MISO.MDIO1_MDIO.I2C_EMAC1_SDA.NAND_ADO10.SDMMC_DATA4.EMAC2_RXD0	HPS_I0B_19			M3				
HPS			HPS_I0B_20		GPIO1_I019.SPIM0_S50_N.MDIO1_MDC.I2C_EMAC1_SCL.NAND_ADO11.SDMMC_DATA5.EMAC2_RXD1	HPS_I0B_20			AC11				
HPS			HPS_I0B_21		GPIO1_I020.SPIM0_CLK.SPIS1_CLK.I2C_EMAC2_SDA.NAND_ADO12.SDMMC_DATA6.EMAC2_TXD2	HPS_I0B_21			AC12				
HPS			HPS_I0B_22		GPIO1_I021.SPIM0_MOSI.SPIS1_MOSI.I2C_EMAC2_SCL.NAND_ADO13.SDMMC_DATA7.EMAC2_TXD3	HPS_I0B_22			H3				
HPS			HPS_I0B_23		GPIO1_I022.SPIM0_MISO.SPIS1_S50_N.MDIO0_MDIO.I2C_EMAC0_SDA.NAND_ADO14.EMAC2_RXD2	HPS_I0B_23			AD13				
HPS			HPS_I0B_24		GPIO1_I023.SPIM0_S50_N.SPIS1_MISO.MDIO0_MDC.I2C_EMAC0_SCL.NAND_ADO15.EMAC2_RXD3	HPS_I0B_24			F3				
9A			GXER9A_TX_CH0p						AK1				
9A			GXER9A_TX_CH1p						AL4				
9A			GXER9A_TX_CH2p						AP1				
9A			GXER9A_TX_CH3p						AR4				
9A			GXER9A_TX_CH8p						AV1				
9A			GXER9A_TX_CH9p						AW4				
9A			GXER9A_TX_CH10p						BB1				
9A			GXER9A_TX_CH11p						BC4				
9A			GXER9A_TX_CH12p						BF1				
9A			GXER9A_TX_CH13p						BG4				
9A			GXER9A_TX_CH14p						BK1				
9A			GXER9A_TX_CH15p						BL4				
9A			GXER9A_TX_CH20p						BP1				
9A			GXER9A_TX_CH21p						BR4				
9A			GXER9A_TX_CH22p						BV1				
9A			GXER9A_TX_CH23p						BW4				
9A			GXER9A_TX_CH0n						AJ2				
9A			GXER9A_TX_CH1n						AM5				
9A			GXER9A_TX_CH2n						AN2				
9A			GXER9A_TX_CH3n						AT5				
9A			GXER9A_TX_CH8n						AU2				
9A			GXER9A_TX_CH9n						AY5				
9A			GXER9A_TX_CH10n						BA2				
9A			GXER9A_TX_CH11n						BD5				
9A			GXER9A_TX_CH12n						BE2				
9A			GXER9A_TX_CH13n						BH5				
9A			GXER9A_TX_CH14n						BJ2				
9A			GXER9A_TX_CH15n						BM5				
9A			GXER9A_TX_CH20n						BN2				
9A			GXER9A_TX_CH21n						BT5				
9A			GXER9A_TX_CH22n						BU2				
9A			GXER9A_TX_CH23n						BV5				
9A			GXER9A_RX_CH0p						AK7				
9A			GXER9A_RX_CH1p						AL10				
9A			GXER9A_RX_CH2p						AP7				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
9A			GXER9A_RX_CH3p						AR10				
9A			GXER9A_RX_CH8p						AV7				
9A			GXER9A_RX_CH9p						AW10				
9A			GXER9A_RX_CH10p						BB7				
9A			GXER9A_RX_CH11p						BC10				
9A			GXER9A_RX_CH12p						BF7				
9A			GXER9A_RX_CH13p						BG10				
9A			GXER9A_RX_CH14p						BK7				
9A			GXER9A_RX_CH15p						BL10				
9A			GXER9A_RX_CH20p						BP7				
9A			GXER9A_RX_CH21p						BR10				
9A			GXER9A_RX_CH22p						BV7				
9A			GXER9A_RX_CH23p						BW10				
9A			GXER9A_RX_CH0n						AJ8				
9A			GXER9A_RX_CH1n						AM11				
9A			GXER9A_RX_CH2n						AN8				
9A			GXER9A_RX_CH3n						AT11				
9A			GXER9A_RX_CH8n						AJ8				
9A			GXER9A_RX_CH9n						AV11				
9A			GXER9A_RX_CH10n						BA8				
9A			GXER9A_RX_CH11n						BD11				
9A			GXER9A_RX_CH12n						BE8				
9A			GXER9A_RX_CH13n						BH11				
9A			GXER9A_RX_CH14n						BJ8				
9A			GXER9A_RX_CH15n						BM11				
9A			GXER9A_RX_CH20n						BN8				
9A			GXER9A_RX_CH21n						BT11				
9A			GXER9A_RX_CH22n						BU8				
9A			GXER9A_RX_CH23n						BV11				
9A			REFCLK_GXER9A_CH0p						AT13				
9A			REFCLK_GXER9A_CH0n						AP13				
9A			REFCLK_GXER9A_CH1p						AR14				
9A			REFCLK_GXER9A_CH1n						AN14				
9A			REFCLK_GXER9A_CH2p						AJ12				
9A			REFCLK_GXER9A_CH2n						AH11				
9A			REFCLK_GXER9A_CH3p						AK13				
9A			REFCLK_GXER9A_CH3n						AH13				
9A			REFCLK_GXER9A_CH4p						AJ14				
9A			REFCLK_GXER9A_CH4n						AI14				
9A			REFCLK_GXER9A_CH5p						AR16				
9A			REFCLK_GXER9A_CH5n						AN16				
9A			REFCLK_GXER9A_CH6p						AJ16				
9A			REFCLK_GXER9A_CH6n						AI16				
9A			REFCLK_GXER9A_CH7p						AH15				
9A			REFCLK_GXER9A_CH7n						AK15				
9A			REFCLK_GXER9A_CH8p						AH17				
9A			REFCLK_GXER9A_CH8n						AK17				
10A			GXPL10A_TX_CH0p						BP55				
10A			GXPL10A_TX_CH1p						BN52				
10A			GXPL10A_TX_CH2p						BK55				
10A			GXPL10A_TX_CH3p						BJ52				
10A			GXPL10A_TX_CH4p						BF55				
10A			GXPL10A_TX_CH5p						BE52				
10A			GXPL10A_TX_CH6p						BB55				
10A			GXPL10A_TX_CH7p						BA52				
10A			GXPL10A_TX_CH8p						AV55				
10A			GXPL10A_TX_CH9p						AU52				
10A			GXPL10A_TX_CH10p						AP55				
10A			GXPL10A_TX_CH11p						AN52				
10A			GXPL10A_TX_CH12p						AK55				
10A			GXPL10A_TX_CH13p						AJ52				
10A			GXPL10A_TX_CH14p						AF55				
10A			GXPL10A_TX_CH15p						AE52				
10A			GXPL10A_TX_CH0n						BR56				
10A			GXPL10A_TX_CH1n						BM53				
10A			GXPL10A_TX_CH2n						BL56				
10A			GXPL10A_TX_CH3n						BK53				
10A			GXPL10A_TX_CH4n						BB56				
10A			GXPL10A_TX_CH5n						BD53				
10A			GXPL10A_TX_CH6n						BC56				
10A			GXPL10A_TX_CH7n						AV53				
10A			GXPL10A_TX_CH8n						AW56				
10A			GXPL10A_TX_CH9n						AT53				
10A			GXPL10A_TX_CH10n						AR56				
10A			GXPL10A_TX_CH11n						AM53				
10A			GXPL10A_TX_CH12n						AL56				
10A			GXPL10A_TX_CH13n						AH53				
10A			GXPL10A_TX_CH14n						AG56				
10A			GXPL10A_TX_CH15n						AD53				
10A			GXPL10A_RX_CH0p						BP61				
10A			GXPL10A_RX_CH1p						BN58				
10A			GXPL10A_RX_CH2p						BK61				
10A			GXPL10A_RX_CH3p						BJ58				
10A			GXPL10A_RX_CH4p						BF61				
10A			GXPL10A_RX_CH5p						BE58				
10A			GXPL10A_RX_CH6p						BB61				
10A			GXPL10A_RX_CH7p						BA58				
10A			GXPL10A_RX_CH8p						AV61				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	B2A4	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
10A			GXPL10A_RX_CH9p						AU58				
10A			GXPL10A_RX_CH10p						AF61				
10A			GXPL10A_RX_CH11p						AN58				
10A			GXPL10A_RX_CH12p						AK61				
10A			GXPL10A_RX_CH13p						AJ58				
10A			GXPL10A_RX_CH14p						AF61				
10A			GXPL10A_RX_CH15p						AE58				
10A			GXPL10A_RX_CH0n						BS62				
10A			GXPL10A_RX_CH1n						BN59				
10A			GXPL10A_RX_CH2n						BL62				
10A			GXPL10A_RX_CH3n						BH59				
10A			GXPL10A_RX_CH4n						BG62				
10A			GXPL10A_RX_CH5n						BD59				
10A			GXPL10A_RX_CH6n						BC62				
10A			GXPL10A_RX_CH7n						AY59				
10A			GXPL10A_RX_CH8n						AW62				
10A			GXPL10A_RX_CH9n						AT59				
10A			GXPL10A_RX_CH10n						AR62				
10A			GXPL10A_RX_CH11n						AN59				
10A			GXPL10A_RX_CH12n						AL62				
10A			GXPL10A_RX_CH13n						AH59				
10A			GXPL10A_RX_CH14n						AG62				
10A			GXPL10A_RX_CH15n						AD59				
U10 WHR			I_PIN_PERST_N_U10_P						BU58				
10A			REFCLK_GXPL10A_CH0n						AH49				
10A			REFCLK_GXPL10A_CH0p						AJ48				
10A			REFCLK_GXPL10A_CH2n						AD49				
10A			REFCLK_GXPL10A_CH2p						AE48				
10A			GND						AV59				
			GND						BN48				
			GND						BV57				
			GND						BU54				
			GND						Y9				
			GND						Y7				
			GND						Y61				
			GND						Y59				
			GND						Y57				
			GND						Y55				
			GND						Y53				
			GND						Y51				
			GND						Y5				
			GND						Y49				
			GND						Y47				
			GND						Y45				
			GND						Y43				
			GND						Y41				
			GND						Y39				
			GND						Y37				
			GND						Y35				
			GND						Y33				
			GND						Y31				
			GND						Y3				
			GND						Y29				
			GND						Y27				
			GND						Y25				
			GND						Y23				
			GND						Y21				
			GND						Y19				
			GND						Y17				
			GND						Y15				
			GND						Y13				
			GND						Y11				
			GND						Y1				
			GND						W60				
			GND						W46				
			GND						W4				
			GND						W32				
			GND						W18				
			GND						V53				
			GND						V39				
			GND						V25				
			GND						V11				
			GND						U60				
			GND						U46				
			GND						U4				
			GND						U32				
			GND						U18				
			GND						T53				
			GND						T39				
			GND						T25				
			GND						T11				
			GND						R8				
			GND						R62				
			GND						R60				
			GND						R6				
			GND						RS8				
			GND						RS6				
			GND						RS4				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						R52				
			GND						R50				
			GND						R48				
			GND						R46				
			GND						R44				
			GND						R42				
			GND						R40				
			GND						R4				
			GND						R38				
			GND						R36				
			GND						R34				
			GND						R32				
			GND						R30				
			GND						R28				
			GND						R26				
			GND						R24				
			GND						R22				
			GND						R20				
			GND						R2				
			GND						R18				
			GND						R16				
			GND						R14				
			GND						R12				
			GND						R10				
			GND						P53				
			GND						P39				
			GND						P25				
			GND						P11				
			GND						N60				
			GND						N46				
			GND						N4				
			GND						N32				
			GND						N18				
			GND						M53				
			GND						M39				
			GND						M25				
			GND						M11				
			GND						L60				
			GND						L46				
			GND						L4				
			GND						L32				
			GND						L18				
			GND						K9				
			GND						K7				
			GND						K61				
			GND						K59				
			GND						K57				
			GND						K55				
			GND						K53				
			GND						K51				
			GND						K5				
			GND						K49				
			GND						K47				
			GND						K45				
			GND						K43				
			GND						K41				
			GND						K39				
			GND						K37				
			GND						K35				
			GND						K33				
			GND						K31				
			GND						K3				
			GND						K29				
			GND						K27				
			GND						K25				
			GND						K23				
			GND						K21				
			GND						K19				
			GND						K17				
			GND						K15				
			GND						K13				
			GND						K11				
			GND						K1				
			GND						J60				
			GND						J46				
			GND						J4				
			GND						J32				
			GND						J18				
			GND						H53				
			GND						H39				
			GND						H25				
			GND						H11				
			GND						G60				
			GND						G46				
			GND						G4				
			GND						G32				
			GND						G18				
			GND						F53				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						F39				
			GND						F25				
			GND						F11				
			GND						E8				
			GND						E62				
			GND						E60				
			GND						E6				
			GND						E58				
			GND						E56				
			GND						E54				
			GND						E52				
			GND						E50				
			GND						E48				
			GND						E46				
			GND						E44				
			GND						E42				
			GND						E40				
			GND						E4				
			GND						E38				
			GND						E36				
			GND						E34				
			GND						E32				
			GND						E30				
			GND						E28				
			GND						E26				
			GND						E24				
			GND						E22				
			GND						E20				
			GND						E2				
			GND						E18				
			GND						E16				
			GND						E14				
			GND						E12				
			GND						E10				
			GND						DC60				
			GND						DC58				
			GND						DC44				
			GND						DC4				
			GND						DC30				
			GND						DC2				
			GND						DC16				
			GND						DB9				
			GND						DB61				
			GND						DB59				
			GND						DB51				
			GND						DB37				
			GND						DB3				
			GND						DB23				
			GND						DB1				
			GND						DA62				
			GND						DA60				
			GND						DA58				
			GND						DA44				
			GND						DA30				
			GND						DA2				
			GND						DA16				
			GND						D61				
			GND						D53				
			GND						D39				
			GND						D3				
			GND						D25				
			GND						D11				
			GND						D1				
			GND						CY9				
			GND						CY61				
			GND						CY51				
			GND						CY37				
			GND						CY23				
			GND						CY1				
			GND						CW8				
			GND						CW62				
			GND						CW60				
			GND						CW6				
			GND						CW58				
			GND						CW56				
			GND						CW54				
			GND						CW52				
			GND						CW50				
			GND						CW48				
			GND						CW46				
			GND						CW44				
			GND						CW42				
			GND						CW40				
			GND						CW4				
			GND						CW38				
			GND						CW36				
			GND						CW34				
			GND						CW32				





Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						C48				
			GND						C46				
			GND						C44				
			GND						C42				
			GND						C40				
			GND						C4				
			GND						C38				
			GND						C36				
			GND						C34				
			GND						C32				
			GND						C30				
			GND						C28				
			GND						C26				
			GND						C24				
			GND						C22				
			GND						C20				
			GND						C2				
			GND						C18				
			GND						C16				
			GND						C14				
			GND						C12				
			GND						C10				
			GND						CH9				
			GND						CH51				
			GND						CH37				
			GND						CH23				
			GND						CG58				
			GND						CG44				
			GND						CG30				
			GND						CG2				
			GND						CG16				
			GND						CF9				
			GND						CF51				
			GND						CF37				
			GND						CF23				
			GND						CE58				
			GND						CE44				
			GND						CE30				
			GND						CE2				
			GND						CE16				
			GND						CD9				
			GND						CD7				
			GND						CD61				
			GND						CD59				
			GND						CD57				
			GND						CD55				
			GND						CD53				
			GND						CD51				
			GND						CD5				
			GND						CD49				
			GND						CD47				
			GND						CD45				
			GND						CD43				
			GND						CD41				
			GND						CD39				
			GND						CD37				
			GND						CD35				
			GND						CD33				
			GND						CD31				
			GND						CD3				
			GND						CD29				
			GND						CD27				
			GND						CD25				
			GND						CD23				
			GND						CD21				
			GND						CD19				
			GND						CD17				
			GND						CD15				
			GND						CD13				
			GND						CD11				
			GND						CD1				
			GND						CC6				
			GND						CC54				
			GND						CC46				
			GND						CC44				
			GND						CC42				
			GND						CC40				
			GND						CC4				
			GND						CC12				
			GND						CC10				
			GND						CB9				
			GND						CB55				
			GND						CB5				
			GND						CB3				
			GND						CB23				
			GND						CB11				
			GND						CA8				
			GND						CA6				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						CA46				
			GND						CA44				
			GND						CA42				
			GND						CA40				
			GND						CA4				
			GND						CA38				
			GND						CA36				
			GND						CA34				
			GND						CA32				
			GND						CA30				
			GND						CA28				
			GND						CA26				
			GND						CA24				
			GND						CA22				
			GND						CA20				
			GND						CA2				
			GND						CA12				
			GND						CA10				
			GND						C62				
			GND						C60				
			GND						C46				
			GND						C4				
			GND						C32				
			GND						C2				
			GND						C18				
			GND						BY9				
			GND						BY7				
			GND						BV51				
			GND						BV3				
			GND						BV23				
			GND						BY13				
			GND						BY1				
			GND						BW8				
			GND						BW62				
			GND						BW60				
			GND						BW6				
			GND						BW58				
			GND						BW56				
			GND						BW54				
			GND						BW52				
			GND						BW50				
			GND						BW48				
			GND						BW46				
			GND						BW44				
			GND						BW42				
			GND						BW40				
			GND						BW38				
			GND						BW36				
			GND						BW34				
			GND						BW32				
			GND						BW30				
			GND						BW28				
			GND						BW26				
			GND						BW24				
			GND						BW22				
			GND						BW20				
			GND						BW2				
			GND						BW16				
			GND						BW14				
			GND						BW12				
			GND						BV9				
			GND						BV61				
			GND						BV59				
			GND						BV5				
			GND						BV3				
			GND						BV25				
			GND						BV11				
			GND						BU62				
			GND						BU6				
			GND						BU56				
			GND						BU42				
			GND						BU40				
			GND						BU4				
			GND						BU38				
			GND						BU36				
			GND						BU34				
			GND						BU32				
			GND						BU30				
			GND						BU28				
			GND						BU26				
			GND						BU24				
			GND						BU22				
			GND						BU20				
			GND						BU18				
			GND						BU12				
			GND						BU10				
			GND						BT9				
			GND						BT7				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						BT61				
			GND						BT57				
			GND						BT55				
			GND						BT3				
			GND						BT25				
			GND						BT17				
			GND						BT15				
			GND						BT13				
			GND						BT1				
			GND						BR8				
			GND						BR60				
			GND						BR6				
			GND						BR58				
			GND						BR54				
			GND						BR52				
			GND						BR48				
			GND						BR46				
			GND						BR42				
			GND						BR40				
			GND						BR38				
			GND						BR22				
			GND						BR2				
			GND						BR18				
			GND						BR16				
			GND						BR14				
			GND						BR12				
			GND						BP9				
			GND						BP59				
			GND						BP57				
			GND						BP53				
			GND						BP51				
			GND						BP5				
			GND						BP49				
			GND						BP45				
			GND						BP43				
			GND						BP3				
			GND						BP13				
			GND						BP11				
			GND						BN62				
			GND						BN60				
			GND						BN6				
			GND						BN56				
			GND						BN54				
			GND						BN42				
			GND						BN40				
			GND						BN4				
			GND						BN38				
			GND						BN36				
			GND						BN34				
			GND						BN32				
			GND						BN30				
			GND						BN28				
			GND						BN26				
			GND						BN24				
			GND						BN22				
			GND						BN20				
			GND						BN18				
			GND						BN16				
			GND						BN12				
			GND						BN10				
			GND						BM9				
			GND						BM7				
			GND						BM61				
			GND						BM57				
			GND						BM55				
			GND						BM51				
			GND						BM3				
			GND						BM19				
			GND						BM13				
			GND						BM1				
			GND						BL8				
			GND						BL60				
			GND						BL6				
			GND						BL58				
			GND						BL54				
			GND						BL52				
			GND						BL42				
			GND						BL40				
			GND						BL38				
			GND						BL36				
			GND						BL34				
			GND						BL32				
			GND						BL30				
			GND						BL28				
			GND						BL26				
			GND						BL24				
			GND						BL22				
			GND						BL2				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						BL18				
			GND						BL16				
			GND						BL12				
			GND						BK9				
			GND						BK59				
			GND						BK57				
			GND						BK53				
			GND						BK51				
			GND						BK5				
			GND						BK47				
			GND						BK45				
			GND						BK43				
			GND						BK3				
			GND						BK13				
			GND						BK11				
			GND						BJ62				
			GND						BJ60				
			GND						BJ6				
			GND						BJ56				
			GND						BJ54				
			GND						BJ50				
			GND						BJ48				
			GND						BJ46				
			GND						BJ44				
			GND						BJ42				
			GND						BJ40				
			GND						BJ4				
			GND						BJ38				
			GND						BJ26				
			GND						BJ24				
			GND						BJ22				
			GND						BJ18				
			GND						BJ16				
			GND						BJ12				
			GND						BJ10				
			GND						BH9				
			GND						BH7				
			GND						BH61				
			GND						BH57				
			GND						BH55				
			GND						BH51				
			GND						BH3				
			GND						BH17				
			GND						BH15				
			GND						BH13				
			GND						BH1				
			GND						BG8				
			GND						BG60				
			GND						BG6				
			GND						BG58				
			GND						BG54				
			GND						BG52				
			GND						BG50				
			GND						BG48				
			GND						BG46				
			GND						BG44				
			GND						BG42				
			GND						BG40				
			GND						BG38				
			GND						BG26				
			GND						BG24				
			GND						BG22				
			GND						BG20				
			GND						BG2				
			GND						BG18				
			GND						BG12				
			GND						BF9				
			GND						BF59				
			GND						BF57				
			GND						BF53				
			GND						BF51				
			GND						BF5				
			GND						BF49				
			GND						BF47				
			GND						BF45				
			GND						BF3				
			GND						BF19				
			GND						BF17				
			GND						BF15				
			GND						BF13				
			GND						BF11				
			GND						BE62				
			GND						BE60				
			GND						BE6				
			GND						BE56				
			GND						BE54				
			GND						BE50				
			GND						BE48				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						BE42				
			GND						BE40				
			GND						BE4				
			GND						BE38				
			GND						BE26				
			GND						BE24				
			GND						BE22				
			GND						BE18				
			GND						BE12				
			GND						BE10				
			GND						BD9				
			GND						BD7				
			GND						BD61				
			GND						BD57				
			GND						BD55				
			GND						BD51				
			GND						BD47				
			GND						BD45				
			GND						BD3				
			GND						BD17				
			GND						BD15				
			GND						BD13				
			GND						BD1				
			GND						BC8				
			GND						BC60				
			GND						BC6				
			GND						BC58				
			GND						BC54				
			GND						BC52				
			GND						BC50				
			GND						BC42				
			GND						BC40				
			GND						BC38				
			GND						BC26				
			GND						BC24				
			GND						BC22				
			GND						BC2				
			GND						BC18				
			GND						BC16				
			GND						BC12				
			GND						BB9				
			GND						BB59				
			GND						BB57				
			GND						BB53				
			GND						BB51				
			GND						BB5				
			GND						BB47				
			GND						BB45				
			GND						BB43				
			GND						BB3				
			GND						BB13				
			GND						BB11				
			GND						BAG2				
			GND						BAG0				
			GND						BA6				
			GND						BA56				
			GND						BA54				
			GND						BA50				
			GND						BA48				
			GND						BA46				
			GND						BA42				
			GND						BA40				
			GND						BA4				
			GND						BA38				
			GND						BA24				
			GND						BA22				
			GND						BA20				
			GND						BA18				
			GND						BA16				
			GND						BA12				
			GND						BA10				
			GND						B61				
			GND						B59				
			GND						B53				
			GND						B39				
			GND						B3				
			GND						B25				
			GND						B11				
			GND						B1				
			GND						AY9				
			GND						AY7				
			GND						AY61				
			GND						AY57				
			GND						AY55				
			GND						AY51				
			GND						AY45				
			GND						AY3				
			GND						AY13				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						AY1				
			GND						AW8				
			GND						AW60				
			GND						AW6				
			GND						AW58				
			GND						AW54				
			GND						AW52				
			GND						AW50				
			GND						AW48				
			GND						AW42				
			GND						AW40				
			GND						AW38				
			GND						AW26				
			GND						AW24				
			GND						AW22				
			GND						AW2				
			GND						AW18				
			GND						AW16				
			GND						AW12				
			GND						AV9				
			GND						AV59				
			GND						AV57				
			GND						AV53				
			GND						AV51				
			GND						AV5				
			GND						AV45				
			GND						AV3				
			GND						AV19				
			GND						AV17				
			GND						AV13				
			GND						AV11				
			GND						AU62				
			GND						AU60				
			GND						AU6				
			GND						AU56				
			GND						AU54				
			GND						AU50				
			GND						AU48				
			GND						AU42				
			GND						AU40				
			GND						AU4				
			GND						AU38				
			GND						AU26				
			GND						AU24				
			GND						AU22				
			GND						AU18				
			GND						AU16				
			GND						AU14				
			GND						AU12				
			GND						AU10				
			GND						AT9				
			GND						AT7				
			GND						AT61				
			GND						AT57				
			GND						AT55				
			GND						AT51				
			GND						AT47				
			GND						AT45				
			GND						AT43				
			GND						AT3				
			GND						AT15				
			GND						AT1				
			GND						AR8				
			GND						AR60				
			GND						AR6				
			GND						AR58				
			GND						AR54				
			GND						AR52				
			GND						AR50				
			GND						AR46				
			GND						AR42				
			GND						AR40				
			GND						AR38				
			GND						AR26				
			GND						AR24				
			GND						AR22				
			GND						AR2				
			GND						AR12				
			GND						AP9				
			GND						AP59				
			GND						AP57				
			GND						AP53				
			GND						AP51				
			GND						AP5				
			GND						AP47				
			GND						AP45				
			GND						AP43				
			GND						AP3				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R2A4	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						AP19				
			GND						AP15				
			GND						AP11				
			GND						AN62				
			GND						AN60				
			GND						AN6				
			GND						AN56				
			GND						AN54				
			GND						AN50				
			GND						AN44				
			GND						AN42				
			GND						AN40				
			GND						AN4				
			GND						AN38				
			GND						AN36				
			GND						AN34				
			GND						AN32				
			GND						AN30				
			GND						AN28				
			GND						AN26				
			GND						AN24				
			GND						AN22				
			GND						AN12				
			GND						AN10				
			GND						AM9				
			GND						AM7				
			GND						AM61				
			GND						AM57				
			GND						AM55				
			GND						AM51				
			GND						AM47				
			GND						AM45				
			GND						AM43				
			GND						AM3				
			GND						AM17				
			GND						AM15				
			GND						AM13				
			GND						AM1				
			GND						AL8				
			GND						AL60				
			GND						AL6				
			GND						AL58				
			GND						AL54				
			GND						AL52				
			GND						AL50				
			GND						AL48				
			GND						AL46				
			GND						AL44				
			GND						AL42				
			GND						AL40				
			GND						AL38				
			GND						AL36				
			GND						AL34				
			GND						AL32				
			GND						AL30				
			GND						AL28				
			GND						AL26				
			GND						AL24				
			GND						AL22				
			GND						AL20				
			GND						AL2				
			GND						AL18				
			GND						AL12				
			GND						AK9				
			GND						AK59				
			GND						AK57				
			GND						AK53				
			GND						AK51				
			GND						AK5				
			GND						AK49				
			GND						AK47				
			GND						AK45				
			GND						AK43				
			GND						AK3				
			GND						AK19				
			GND						AK11				
			GND						AJ62				
			GND						AJ60				
			GND						AJ6				
			GND						AJ56				
			GND						AJ54				
			GND						AJ50				
			GND						AJ44				
			GND						AJ42				
			GND						AJ40				
			GND						AJ4				
			GND						AJ38				
			GND						AJ22				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						AJ18				
			GND						AJ10				
			GND						AH9				
			GND						AH7				
			GND						AH61				
			GND						AH57				
			GND						AH55				
			GND						AH51				
			GND						AH47				
			GND						AH39				
			GND						AH3				
			GND						AH1				
			GND						AG8				
			GND						AG60				
			GND						AG58				
			GND						AG54				
			GND						AG52				
			GND						AG50				
			GND						AG48				
			GND						AG44				
			GND						AG42				
			GND						AG40				
			GND						AG38				
			GND						AG36				
			GND						AG34				
			GND						AG32				
			GND						AG30				
			GND						AG28				
			GND						AG26				
			GND						AG24				
			GND						AG22				
			GND						AG20				
			GND						AG2				
			GND						AG18				
			GND						AG16				
			GND						AG14				
			GND						AG12				
			GND						AG10				
			GND						AF59				
			GND						AF57				
			GND						AF53				
			GND						AF51				
			GND						AF49				
			GND						AF47				
			GND						AF45				
			GND						AF39				
			GND						AF17				
			GND						AF15				
			GND						AF13				
			GND						AF11				
			GND						AE8				
			GND						AE62				
			GND						AE60				
			GND						AE6				
			GND						AE56				
			GND						AE54				
			GND						AE50				
			GND						AE46				
			GND						AE42				
			GND						AE40				
			GND						AE4				
			GND						AE38				
			GND						AE36				
			GND						AE34				
			GND						AE32				
			GND						AE30				
			GND						AE28				
			GND						AE26				
			GND						AE24				
			GND						AE22				
			GND						AE20				
			GND						AE2				
			GND						AE18				
			GND						AE16				
			GND						AE14				
			GND						AE12				
			GND						AE10				
			GND						AD61				
			GND						AD57				
			GND						AD55				
			GND						AD51				
			GND						AD47				
			GND						AD41				
			GND						AC60				
			GND						AC58				
			GND						AC54				
			GND						AC52				
			GND						AC50				



Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			GND						AC48				
			GND						AC40				
			GND						AC38				
			GND						AC36				
			GND						AC34				
			GND						AC32				
			GND						AC30				
			GND						AC28				
			GND						AC26				
			GND						AC24				
			GND						AC20				
			GND						AB59				
			GND						AB57				
			GND						AB53				
			GND						AB51				
			GND						AB49				
			GND						AB41				
			GND						AB21				
			GND						AA60				
			GND						AA58				
			GND						AA54				
			GND						AA50				
			GND						AA40				
			GND						A60				
			GND						A58				
			GND						A46				
			GND						A4				
			GND						A32				
			GND						A2				
			GND						A18				
			GNDSENSE						BA26				
			VCC						BV23				
			VCC						BV21				
			VCC						BT23				
			VCC						BT21				
			VCC						BP41				
			VCC						BP39				
			VCC						BP37				
			VCC						BP35				
			VCC						BP33				
			VCC						BP31				
			VCC						BP29				
			VCC						BP27				
			VCC						BP25				
			VCC						BP23				
			VCC						BP21				
			VCC						BM41				
			VCC						BM39				
			VCC						BM37				
			VCC						BM35				
			VCC						BM33				
			VCC						BM31				
			VCC						BM29				
			VCC						BM27				
			VCC						BM25				
			VCC						BM23				
			VCC						BM21				
			VCC						BK41				
			VCC						BK39				
			VCC						BK37				
			VCC						BK25				
			VCC						BK23				
			VCC						BK21				
			VCC						BH41				
			VCC						BH39				
			VCC						BH37				
			VCC						BH25				
			VCC						BH23				
			VCC						BH21				
			VCC						BF41				
			VCC						BF39				
			VCC						BF37				
			VCC						BF25				
			VCC						BF23				
			VCC						BF21				
			VCC						BD41				
			VCC						BD39				
			VCC						BD37				
			VCC						BD25				
			VCC						BD23				
			VCC						BD21				
			VCC						BB41				
			VCC						BB39				
			VCC						BB37				
			VCC						BB23				
			VCC						BB21				
			VCC						AY41				
			VCC						AY39				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			VCC						AY37				
			VCC						AY25				
			VCC						AY23				
			VCC						AY21				
			VCC						AV41				
			VCC						AV39				
			VCC						AV37				
			VCC						AV25				
			VCC						AV23				
			VCC						AV21				
			VCC						AT41				
			VCC						AT39				
			VCC						AT37				
			VCC						AT25				
			VCC						AT23				
			VCC						AT21				
			VCC						AP41				
			VCC						AP39				
			VCC						AP37				
			VCC						AP25				
			VCC						AP23				
			VCC						AP21				
			VCC						AM41				
			VCC						AM39				
			VCC						AM37				
			VCC						AM35				
			VCC						AM33				
			VCC						AM31				
			VCC						AM29				
			VCC						AM27				
			VCC						AM25				
			VCC						AM23				
			VCC						AM21				
			VCC						AK41				
			VCC						AK39				
			VCC						AK37				
			VCC						AK35				
			VCC						AK33				
			VCC						AK31				
			VCC						AK29				
			VCC						AK27				
			VCC						AK25				
			VCC						AK21				
			VCC						AH41				
			VCC						AF41				
			VCCPT						BR36				
			VCCPT						BR34				
			VCCPT						BR32				
			VCCPT						BR30				
			VCCPT						AJ36				
			VCCPT						AJ34				
			VCCPT						AJ32				
			VCCPT						AJ30				
			DNLU						BY55				
			DNLU						CF61				
			DNLU						CH61				
			DNLU						CB53				
			DNLU						BY53				
			DNLU						AH19				
			DNLU						AJ20				
			DNLU						CB61				
			DNLU						AD45				
			DNLU						BW18				
			DNLU						AD19				
			DNLU						BY61				
			DNLU						AE44				
			DNLU						BY19				
			DNLU						AF19				
			DNLU						AM19				
			DNLU						AT19				
			DNLU						AP17				
			DNLU						AT17				
			DNLU						AJ46				
			DNLU						BN50				
			DNLU						BK49				
			DNLU						BM49				
			DNLU						BL48				
			DNLU						BL44				
			DNLU						BLU44				
			DNLU						BT49				
			DNLU						BV49				
			DNLU						BV51				
			DNLU						BU50				
			DNLU						BT51				
			DNLU						BL46				
			DNLU						BR50				
			DNLU						BN46				
			DNLU						BV53				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			DN1						BP47				
			DN1						BN44				
			DN1						BU52				
			DN1						BV55				
			DN1						BT53				
			DN1						BM47				
			DN1						BV45				
			DN1						BN45				
			DN1						BT45				
			DN1						BU46				
			DN1						BU48				
			DN1						BV47				
			DN1						BT47				
			DN1						BL50				
			TEMPDIODE0An						CA52				
			TEMPDIODE0Ap						CC52				
			TEMPDIODE0Cn						AH43				
			TEMPDIODE0Cp						AF43				
			TEMPDIODE3n						AG46				
			TEMPDIODE1p						AH45				
			TEMPDIODE4n						AR20				
			TEMPDIODE4p						AN18				
			VCCBAT						CC48				
			VCCBAT						CA48				
			VCCA_PLL						CC34				
			VCCA_PLL						CC32				
			VCCA_PLL						CC30				
			VCCA_PLL						CC28				
			VCCA_PLL						AA34				
			VCCA_PLL						AA32				
			VCCA_PLL						AA30				
			VCCA_PLL						AA28				
			VCCIO_HPS						AH21				
			VCCIO_HPS						AF21				
			VCCIO_HPS						AD21				
			VCCIO_PIO_2A						CB39				
			VCCIO_PIO_2A						CB37				
			VCCIO_PIO_2A						BY39				
			VCCIO_PIO_2A						BY37				
			VCCIO_PIO_2B						CB35				
			VCCIO_PIO_2B						CB33				
			VCCIO_PIO_2B						BY35				
			VCCIO_PIO_2B						BY33				
			VCCIO_PIO_2C						CB31				
			VCCIO_PIO_2C						CB29				
			VCCIO_PIO_2C						BY31				
			VCCIO_PIO_2C						BY29				
			VCCIO_PIO_2D						CB27				
			VCCIO_PIO_2D						CB25				
			VCCIO_PIO_2D						BY27				
			VCCIO_PIO_2D						BY25				
			VCCIO_PIO_3A						AD39				
			VCCIO_PIO_3A						AD37				
			VCCIO_PIO_3A						AB39				
			VCCIO_PIO_3A						AB37				
			VCCIO_PIO_3B						AD35				
			VCCIO_PIO_3B						AD33				
			VCCIO_PIO_3B						AB35				
			VCCIO_PIO_3B						AB33				
			VCCIO_PIO_3C						AD31				
			VCCIO_PIO_3C						AD29				
			VCCIO_PIO_3C						AB31				
			VCCIO_PIO_3C						AB29				
			VCCIO_PIO_3D						AD27				
			VCCIO_PIO_3D						AD25				
			VCCIO_PIO_3D						AB27				
			VCCIO_PIO_3D						AB25				
			VCCIO_PIO_SDM						AV43				
			VCCIO_SDM						AV41				
			VCCIO_SDM						AV41				
2A		VREFB2AND	VREFB2AND						CC38				
2B		VREFB2BND	VREFB2BND						CC36				
2C		VREFB2CND	VREFB2CND						CC26				
2D		VREFB2DND	VREFB2DND						CC24				
3A		VREFB3AND	VREFB3AND						AA38				
3B		VREFB3BND	VREFB3BND						AA36				
3C		VREFB3CND	VREFB3CND						AA26				
3D		VREFB3DND	VREFB3DND						AA24				
		VREFN_ADC	VREFN_ADC						CA62				
		VREFP_ADC	VREFP_ADC						CC62				
		NC	NC						W62				
		NC	NC						V61				
		NC	NC						U62				
		NC	NC						T61				
		NC	NC						P61				
		NC	NC						N62				
		NC	NC						M61				
		NC	NC						L62				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			NC						J62				
			NC						G62				
			NC						CM61				
			NC						CM1				
			NC						CK61				
			NC						CK1				
			NC						CH1				
			NC						CF1				
			NC						CC8				
			NC						CC50				
			NC						CC22				
			NC						CC20				
			NC						CC2				
			NC						CC18				
			NC						CC16				
			NC						CC14				
			NC						CB7				
			NC						CB51				
			NC						CB21				
			NC						CB19				
			NC						CB17				
			NC						CB15				
			NC						CB13				
			NC						CB1				
			NC						CA56				
			NC						CA50				
			NC						CA18				
			NC						CA16				
			NC						CA14				
			NC						BV57				
			NC						BV21				
			NC						BV17				
			NC						BV15				
			NC						BV19				
			NC						BV17				
			NC						BV15				
			NC						BV13				
			NC						BU60				
			NC						BU16				
			NC						BU14				
			NC						BT59				
			NC						AD43				
			NC						AD17				
			NC						AD15				
			NC						AC62				
			NC						AC56				
			NC						AC46				
			NC						AC44				
			NC						AC42				
			NC						AC18				
			NC						AC16				
			NC						AC14				
			NC						AB61				
			NC						AB55				
			NC						AB47				
			NC						AB45				
			NC						AB43				
			NC						AB19				
			NC						AB17				
			NC						AB15				
			NC						AB13				
			NC						AB11				
			NC						AA62				
			NC						AA56				
			NC						AA52				
			NC						AA48				
			NC						AA46				
			NC						AA44				
			NC						AA42				
			NC						AA20				
			NC						AA18				
			NC						AA16				
			NC						AA14				
			NC						AA12				
			NC						AA10				
			VCCRCORE_NC						BV37				
			VCCRCORE_NC						BT37				
			VCCRCORE_NC						BR28				
			VCCRCORE_NC						BR26				
			VCCRCORE_NC						BR24				
			VCCRCORE_NC						AJ28				
			VCCRCORE_NC						AJ26				
			VCCRCORE_NC						AJ24				
			VCCRCORE_NC						AH37				
			VCCRCORE_NC						AF37				
			VCCR1PLL_GXER1						BG16				
			VCCR1PLL_GXER1						BE16				
			VCCRT_GXER1						BP15				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			VCCRT_GXER1						BN14				
			VCCRT_GXER1						BM15				
			VCCRT_GXER1						BL14				
			VCCRT_GXER1						BK15				
			VCCRT_GXER1						BJ14				
			VCCRT_GXER1						BG14				
			VCCRT_GXER1						BE14				
			VCCRT_GXER1						BC14				
			VCCRT_GXER1						BD15				
			VCCRT_GXER1						BA14				
			VCCRT_GXER1						AY15				
			VCCRT_GXER1						AW14				
			VCCRT_GXER1						AV15				
			VCCRT_GXPL1						BD49				
			VCCRT_GXPL1						BC48				
			VCCRT_GXPL1						BB49				
			VCCRT_GXPL1						AV49				
			VCCRT_GXPL1						AV49				
			VCCRT_GXPL1						AT49				
			VCCRT_GXPL1						AR48				
			VCCRT_GXPL1						AP49				
			VCCRT_GXPL1						AN48				
			VCCRT_GXPL1						AM49				
			IO_AUX_RREF10_P						BM43				
			IO_AUX_RREF20						AN20				
			VCCADC						CB43				
			VCCADC						BY43				
			VCCCLK_GXER1						AR18				
			VCCCLK_GXPL1						BH47				
			VCCCLK_GXPL1						BH45				
			VCCFUSEWR_SDM						CB49				
			VCCFUSEWR_SDM						BY49				
			VCCFUSE_GXP						BR44				
			VCCCH						BF43				
			VCCCH						BD43				
			VCCCH						AY43				
			VCCCH						AV43				
			VCCCH						BL20				
			VCCCH						BJ20				
			VCCCH						BE20				
			VCCCH						BC20				
			VCCCH_GXER1						BP17				
			VCCCH_GXER1						BM17				
			VCCCH_GXER1						BK17				
			VCCCH_GXER1						BB17				
			VCCCH_GXER1						AY17				
			VCCCH_GXPL1						BE46				
			VCCCH_GXPL1						BC46				
			VCCCH_GXPL1						AV47				
			VCCCH_GXPL1						AW46				
			VCCCH_GXPL1						AV47				
			VCCCH_GXPL1						AU46				
			VCCCH_GXPL1						AN46				
			VCCCH_SDM						BT43				
			VCCLSENSE						BB25				
			VCCCL_HPS						AK23				
			VCCCL_HPS						AH25				
			VCCCL_HPS						AH23				
			VCCCL_HPS						AF25				
			VCCCL_HPS						AF23				
			VCCCL_SDM						BV41				
			VCCCL_SDM						BV39				
			VCCCL_SDM						BT41				
			VCCCL_SDM						BT39				
			VCCCP						BV35				
			VCCCP						BV33				
			VCCCP						BV31				
			VCCCP						BV29				
			VCCCP						BV27				
			VCCCP						BT35				
			VCCCP						BT33				
			VCCCP						BT31				
			VCCCP						BT29				
			VCCCP						BT27				
			VCCCP						AH35				
			VCCCP						AH33				
			VCCCP						AH31				
			VCCCP						AH29				
			VCCCP						AH27				
			VCCCP						AF35				
			VCCCP						AF33				
			VCCCP						AF31				
			VCCCP						AF29				
			VCCCP						AF27				
			VCCPLLDIG_HPS						AD23				
			VCCPLLDIG_HPS						AB23				
			VCCPLLDIG_SDM						CB45				
			VCCPLLDIG_SDM						BY45				

Bank Number	Index within I/O Bank	VREF	Pin Name/Function	Optional Function(s)	Configuration Function	HPS Function	Dedicated Tx/Rx Channel	Soft CDR Support	R24A	DQS for X4	DQS for X8/X9	DQS for X16/X18	DQS for X32/X36
			VCCPLL_HPS						AC22				
			VCCPLL_HPS						AA22				
			VCCPLL_SDM						CB47				
			VCCPLL_SDM						BY47				
			VCC_HSSI_GXER1						BT19				
			VCC_HSSI_GXER1						BR20				
			VCC_HSSI_GXER1						BP19				
			VCC_HSSI_GXER1						BK19				
			VCC_HSSI_GXER1						BH19				
			VCC_HSSI_GXER1						BD19				
			VCC_HSSI_GXER1						BB19				
			VCC_HSSI_GXER1						AV19				
			VCC_HSSI_GXER1						AW20				
			VCC_HSSI_GXER1						AU20				
			VCC_HSSI_GXPL1						BH43				
			VCC_HSSI_GXPL1						BE44				
			VCC_HSSI_GXPL1						BC44				
			VCC_HSSI_GXPL1						BA44				
			VCC_HSSI_GXPL1						AW44				
			VCC_HSSI_GXPL1						AU44				
			VCC_HSSI_GXPL1						AR44				
			UI0_P_IO_RESREF_0						BH49				

Date	Version	Changes
April 2019	2019.04.02	Initial release.
May 2019	2019.05.29	<ul style="list-style-type: none"> <li>- Updated the GND pins.</li> <li>- Updated the device pin-outs variant from AGF014 to AGFB014.</li> </ul>
September 2019	2019.09.25	<ul style="list-style-type: none"> <li>- Updated the SDM_* pin names to AVST_* pin names on the SDM bank.</li> <li>- Removed NAND_*, CONF_DONE, INIT_DONE, and PWRMGT_PWM0 pins from the SDM bank.</li> </ul>
November 2019	2019.11.01	<p>Updated the following details in the I/O Resource Count tab:</p> <ul style="list-style-type: none"> <li>- Changed LVDS I/O to GPIO</li> <li>- Changed SDM shared LVDS I/O to SDM I/O</li> <li>- Changed HPS shared LVDS I/O (HPS) to HPS I/O</li> <li>- Changed HPS shared LVDS I/O (banks 3C and 3D) to HPS shared GPIO</li> </ul>
December 2019	2019.12.18	<ul style="list-style-type: none"> <li>- Updated the document title to Pin Information for the Intel® Agilex™ AGFB014 ES Device Only.</li> <li>- Updated the following detail in the I/O Resource Count tab: Changed HPS shared GPIO (banks 3C and 3D) to GPIO.</li> </ul>

(1) For more information about pin definition and pin connection guidelines, refer to the [Intel® Agilex™ Device Family Pin Connection Guidelines](#)