

Dedicated Pin	68-Pin PLCC	84-Pin PLCC	100-Pin PQFP
INPUT/GCLK1	67	83	89
INPUT/GCLRn	1	1	91
INPUT/OE1	68	84	90
INPUT/OE2	2	2	92
GND	6, 16, 26, 34, 38, 48, 58, 66	7, 19, 32, 42, 47, 59, 72, 82	13, 28, 40, 45, 61, 76, 88, 97
VCCINT (5.0 V Only)	3, 35	3, 43	41, 93
VCCIO (3.3 V or 5.0 V)	11, 21, 31, 43, 53, 63	13, 26, 38, 53, 66, 78	5, 20, 36, 53, 68, 84
No Connect (N.C.)	–	6, 39, 46, 79	9, 24, 37, 44, 57, 72, 85, 96
Total User I/O Pins (1)	52	64	76

LAB	MC	68-Pin PLCC	84-Pin PLCC	100-Pin PQFP
A	1	13	16	8
A	2	–	–	–
A	3	–	15	7
A	4	12	14	6
A	5	–	–	4
A	6	10	12	3
A	7	–	–	–
A	8	9	11	2
A	9	8	10	1
A	10	–	–	–
A	11	–	9	100
A	12	7	8	99
A	13	–	–	98
A	14	5	5	95
A	15	–	–	–
A	16	4	4	94
B	17	23	28	23
B	18	–	–	–
B	19	22	27	22
B	20	–	–	21
B	21	20	25	19
B	22	–	24	18
B	23	–	–	–
B	24	19	23	17
B	25	18	22	16
B	26	–	–	–
B	27	17	21	15
B	28	–	20	14
B	29	15	18	12
B	30	–	–	11
B	31	–	–	–
B	32	14	17	10
C	33	33	41	39
C	34	–	–	–
C	35	32	40	38
C	36	–	–	35
C	37	30	37	34
C	38	–	36	33
C	39	–	–	–
C	40	29	35	32
C	41	28	34	31
C	42	–	–	–
C	43	27	33	30
C	44	–	–	29
C	45	25	31	27
C	46	–	30	26
C	47	–	–	–
C	48	24	29	25

LAB	MC	68-Pin PLCC	84-Pin PLCC	100-Pin PQFP
D	49	36	44	42
D	50	–	–	–
D	51	37	45	43
D	52	–	–	46
D	53	39	48	47
D	54	–	49	48
D	55	–	–	–
D	56	40	50	49
D	57	41	51	50
D	58	–	–	–
D	59	42	52	51
D	60	–	–	52
D	61	44	54	54
D	62	–	55	55
D	63	–	–	–
D	64	45	56	56
E	65	46	57	58
E	66	–	–	–
E	67	47	58	59
E	68	–	–	60
E	69	49	60	62
E	70	–	61	63
E	71	–	–	–
E	72	50	62	64
E	73	51	63	65
E	74	–	–	–
E	75	52	64	66
E	76	–	65	67
E	77	54	67	69
E	78	–	–	70
E	79	–	–	–
E	80	55	68	71
F	81	56	69	73
F	82	–	–	–
F	83	–	70	74
F	84	57	71	75
F	85	–	–	77
F	86	59	73	78
F	87	–	–	–
F	88	60	74	79
F	89	61	75	80
F	90	–	–	–
F	91	–	76	81
F	92	62	77	82
F	93	–	–	83
F	94	64	80	86
F	95	–	–	–
F	96	65	81	87

Notes:

- (1) The user I/O pin count includes dedicated input pins and all I/O pins.

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